DWG NO	SH	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

ODL-RX Power Supply

VCC C28 10UF C19 0.01UF C27 0.1UF GND

VCC R37 1.0K

U26 HP HFBR-2412 DATA 6 romem_busy

L6 10UH C22 560PF C32 0.01UF C23 0.1UF C31 10UF

VCC C24 10UF GND L5 10UH C20 560PF C29 0.01UF C21 0.1UF C30 10UF

VCC C15 10UF C103 0.1UF C100 0.1UF C102 0.01UF C101 0.01UF GND

taxi_d(8:0)

U22 AM7968-175JC

taxi_d(0) taxi_d(1) taxi_d(2) taxi_d(3) taxi_d(4) taxi_d(5) taxi_d(6) taxi_d(7) taxi_d(8)

VCC C48 1.0UF event_vcc_2 event_vcc_3 GND

SEROUT SEROUT

VCC R29 158 R30 158 R28 267 R34 267 event_data event_data GND

D6 AMP269049-1 200 Mb/s 1300nm FO Transmitter DATA DATA VCC1 VCC2 VEE

U24 DMS connected to GND -> 0 bits VCC -> 5 bits Float -> 10 bits

taxi_dstrb res_event_taxi STRB DMS TLS TSERIN RESET ACK X1 X2 CLK

U42 CY7B991-7JC

TEST FB REF FS PHASE DETECT VCO FILTER Master_Clock

4F0 4F1 3F0 3F1 2F0 2F1 1F0 1F1 SKEW SELECT 4Q0 4Q1 3Q0 3Q1 2Q0 2Q1 1Q0 1Q1

VCCQ1 VCCQ2 VCCN1 VCCN2 VCCN3 VCCN4 PROGRAMMABLE SKEW CLOCK BUFFER (TTL)

VCC R27 res_spare1206 local_clock R26 66.5 GND

VCC R33 res_spare1206 R35 66.5 GND

(MC times 2) txclock

VCC R64 res_spare1206 MC R65 66.5 GND

VCC R31 res_spare1206 rxclock R32 66.5 GND

VCC R38 res_spare1206 txclock2x R39 66.5 GND

VCC C14 10UF C38 0.1UF C39 0.01UF C40 560PF GND

VCC C41 10UF C45 0.1UF C46 0.01UF C47 560PF GND

CDF UPGRADE ELECTRONICS	Fermi National Accelerator Lab		
	TRACER Onboard Clocking and Event data Transmitter TAXIchip		
Theresa M. Shaw	SIZE D	FSNC NO	DWG NO TMS-TRAC-071197-P8 REV B
RD/EED/EES	SCALE		SHEET 8 of 17

E:\JOBS_TRACER\TRACER_PROD_V2\vbdc\tracer.sbk(7;41) Fri May 29 13:54:37 1998