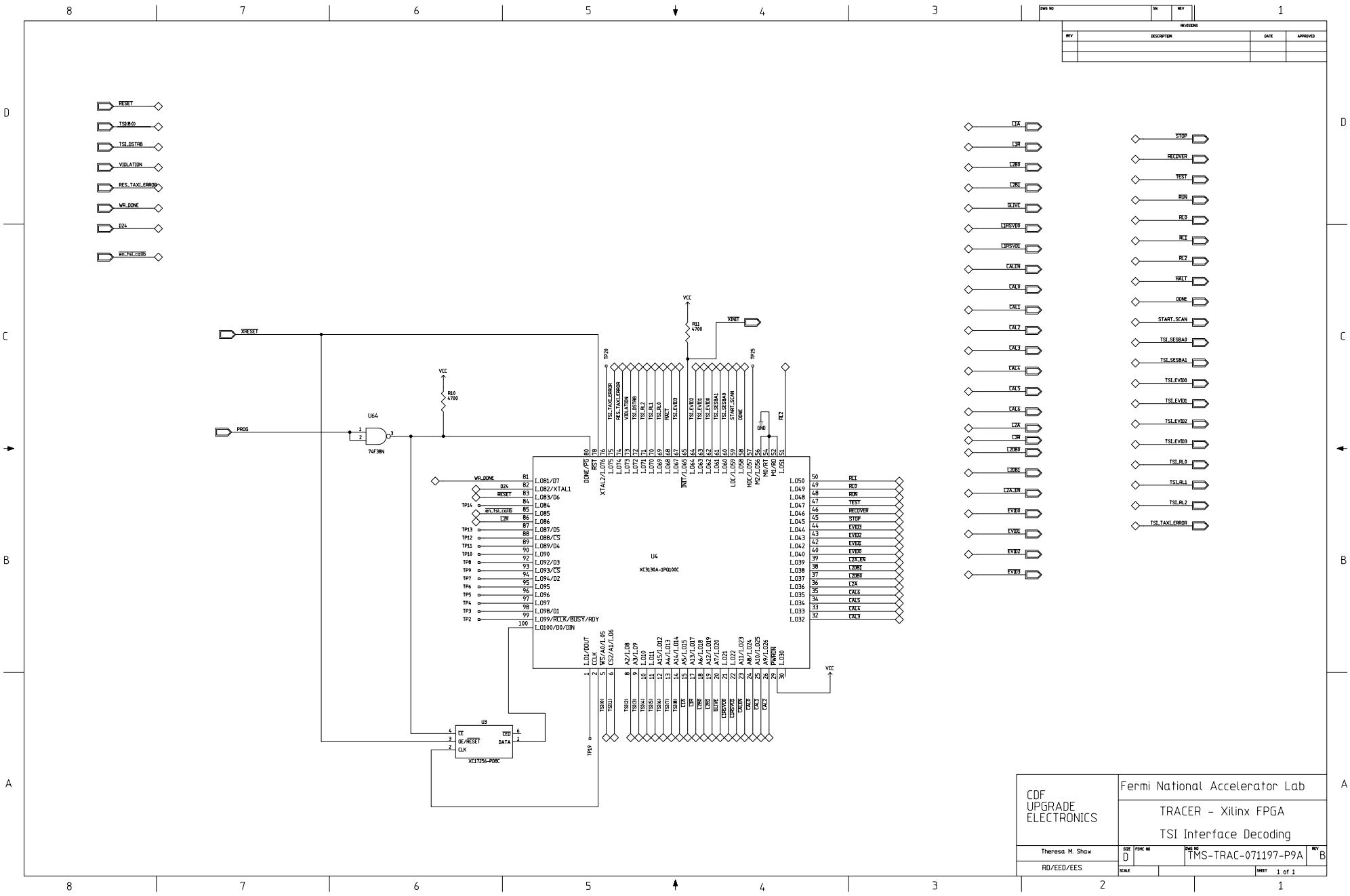

DWG NO	SN	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

CDF UPGRADE ELECTRONICS

Fermi National Accelerator Lab

TRACER - Xilinx FPGA

TSI Interface Decoding

Theresa M. Shaw

RD/EED/EES

SIZE D | FSMC NO | DWG NO TMS-TRAC-071197-P9A | REV B

SCALE | SHEET 1 of 1

E:\JOBS_TRACER\TRACER_PROD_V2\vbdc\tracer.sbk!tsi_interface1(17;31) Fri May 29 13:54:40 1998