| Total No. of LCD Models (All Modes): | 108 |  |  |
| :--- | ---: | :--- | ---: |
| Total No. of LCD Models (On Mode): | 137 |  |  |
|  |  |  |  |
| No. Meet Tier 2 (All Modes): | 30 | No. Meet Sleep Mode: | 59 |
| \% Meet Tier 2 (All Modes): | $28 \%$ | \% Meet Sleep Mode: | $55 \%$ |
| Screen Sizes Meet Tier 2 (All Modes): | $15,17,19,20$ | Screen Sizes Meet Sleep Mode: | $15,17,18,19,20$ |
|  |  |  |  |
| No. Meet On Mode: | 64 | No. Meet Off Mode: | 48 |
| \% Meet On Mode: | $47 \%$ | \% Meet Off Mode: | $44 \%$ |
| Screen Sizes Meet On Mode: | $15,17,18,19,20$ | Screen Sizes Meet Off Mode: | $15,17,19,20$ |


| Model No. | Display Type | Screen Size | Total Pixels | Avg. On | Avg. Sleep | Avg. Off | All Modes Tier 2 | On - <br> Tier 2 | Sleep Tier 2 | Off - <br> Tier 2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | LCD | 17 | 1,310,720 | 25.3 | 3.1 | 0.8 | Fail | Pass | Fail | Pass |
| 2 | LCD | 17 | 1,310,720 | 26.5 | 1.7 | 0.3 | Pass | Pass | Pass | Pass |
| 3 | LCD | 18 | 1,310,720 | 38.6 | 1.6 | 1.1 | Fail | Fail | Pass | Fail |
| 4 | LCD | 19 | 1,310,720 | 34.3 | 2.1 | 2.6 | Fail | Pass | Fail | Fail |
| 5 | LCD | 19 | 1,310,720 | 24.9 | 1.7 | 0.2 | Pass | Pass | Pass | Pass |
| 6 | LCD | 20 | 1,920,000 | 44.3 | 3.4 | 3.4 | Fail | Pass | Fail | Fail |
| 7 | LCD | 20 | 1,920,000 | 40.7 | 3.3 | 0.4 | Fail | Pass | Fail | Pass |
| 8 | LCD | 15 | 786,432 | 26.6 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 9 | LCD | 15 | 924,672 | 30.0 | NR | NR | NI | Fail | NR | NR |
| 10 | LCD | 17 | 1,310,720 | 50.0 | NR | NR | NI | Fail | NR | NR |
| 11 | LCD | 15 | 786,432 | 14.7 | 2.1 | 1.6 | Fail | Pass | Fail | Fail |
| 12 | LCD | 15 | 786,432 | 15.9 | 0.7 | 0.7 | Pass | Pass | Pass | Pass |
| 13 | LCD | 15 | 786,432 | 19.0 | 0.5 | 0.5 | Pass | Pass | Pass | Pass |
| 14 | LCD | 15 | 786,432 | 12.8 | 0.4 | 0.2 | Pass | Pass | Pass | Pass |
| 15 | LCD | 17 | 1,310,720 | 28.8 | 0.7 | 0.7 | Pass | Pass | Pass | Pass |
| 16 | LCD | 17 | 1,310,720 | 17.7 | 0.5 | 0.2 | Pass | Pass | Pass | Pass |
| 17 | LCD | 15 | 924,672 | 25.0 | NR | NR | NI | Pass | NR | NR |
| 18 | LCD | 18 | 1,310,720 | 45.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 19 | LCD | 15 | 786,432 | 19.3 | 1.7 | 1.5 | Fail | Pass | Pass | Fail |
| 20 | LCD | 15 | 786,432 | 25.0 | 1.5 | 1.3 | Fail | Pass | Pass | Fail |
| 21 | LCD | 17 | 1,310,720 | 33.8 | 1.5 | 1.5 | Fail | Pass | Pass | Fail |
| 22 | LCD | 15 | 786,432 | 20.6 | 1.1 | 1.1 | Fail | Pass | Pass | Fail |
| 23 | LCD | 15 | 786,432 | 23.4 | 1.0 | 0.8 | Pass | Pass | Pass | Pass |
| 24 | LCD | 17 | 1,310,720 | 27.9 | 1.0 | 0.8 | Pass | Pass | Pass | Pass |
| 25 | LCD | 17 | 1,310,720 | 27.2 | 0.9 | 0.9 | Pass | Pass | Pass | Pass |
| 26 | LCD | 15 | 786,432 | 19.0 | NR | 0.9 | NI | Pass | NR | Pass |
| 27 | LCD | 15 | 786,432 | 17.7 | 1.4 | 1.3 | Fail | Pass | Pass | Fail |
| 28 | LCD | 15 | 786,432 | 14.1 | 0.7 | 0.5 | Pass | Pass | Pass | Pass |
| 29 | LCD | 17 | 1,310,720 | 27.9 | 1.0 | 0.8 | Pass | Pass | Pass | Pass |
| 30 | LCD | 17 | 1,310,720 | 24.6 | 0.6 | 0.5 | Pass | Pass | Pass | Pass |
| 31 | LCD | 18 | 1,310,720 | 39.4 | 1.3 | 1.2 | Fail | Fail | Pass | Fail |
| 32 | LCD | 19 | 1,310,720 | 31.7 | 1.6 | 0.9 | Pass | Pass | Pass | Pass |
| 33 | LCD | 20.1 | 1,920,000 | 51.5 | 2.8 | 0.9 | Fail | Pass | Fail | Pass |
| 34 | LCD | 20 | 1,920,000 | 48.0 | 1.8 | 1.2 | Fail | Pass | Pass | Fail |
| 35 | LCD | 17 | 1,310,720 | 25.7 | 2.0 | 0.3 | Pass | Pass | Pass | Pass |
| 36 | LCD | 17 | 1,310,720 | 28.5 | 0.8 | 0.3 | Pass | Pass | Pass | Pass |
| 37 | LCD | 15 | 786,432 | 18.0 | 1.4 | 1.4 | Fail | Pass | Pass | Fail |
| 38 | LCD | 18.1 | 1,310,720 | 36.4 | 2.4 | 2.4 | Fail | Pass | Fail | Fail |
| 39 | LCD | 18.1 | 1,310,720 | 42.1 | 0.9 | 0.0 | Fail | Fail | Pass | Pass |

Notes: 1) NR = Not Reported; and 2) NI = Not Included in analysis due to all appropriate values not being reported. 3) Total Number of LCDs has decreased by 6 because wide-screen models are not included in the overall analysis for Tier 2.

| Model No. | Display Type | Screen Size | Total Pixels | Avg. On | Avg. Sleep | Avg. Off | All Modes Tier 2 | On - <br> Tier 2 | Sleep Tier 2 | Off Tier 2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 40 | LCD | 13 | 1,310,720 | 45.0 | NR | NR | NI | Fail | NR | NR |
| 41 | LCD | 14 | 924,672 | 35.0 | NR | NR | NI | Fail | NR | NR |
| 42 | LCD | 15 | 924,672 | 35.0 | NR | NR | NI | Fail | NR | NR |
| 43 | LCD | 15 | 924,672 | 20.0 | 5.0 | 5.0 | Fail | Pass | Fail | Fail |
| 44 | LCD | 15 | 924,672 | 40.0 | NR | NR | NI | Fail | NR | NR |
| 45 | LCD | 15 | 924,672 | 34.0 | 5.0 | 2.0 | Fail | Fail | Fail | Fail |
| 46 | LCD | 15 | 924,672 | 30.0 | NR | NR | NI | Fail | NR | NR |
| 47 | LCD | 15 | 924,672 | 40.0 | 5.0 | 5.0 | Fail | Fail | Fail | Fail |
| 48 | LCD | 15 | 924,672 | 25.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 49 | LCD | 15 | 924,672 | 22.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 50 | LCD | 15 | 924,672 | 45.0 | NR | NR | NI | Fail | NR | NR |
| 51 | LCD | 15 | 1,310,720 | 58.0 | 5.0 | 5.0 | Fail | Fail | Fail | Fail |
| 52 | LCD | 15 | 924,672 | 25.0 | NR | NR | NI | Pass | NR | NR |
| 53 | LCD | 15 | 924,672 | 45.0 | NR | NR | NI | Fail | NR | NR |
| 54 | LCD | 15 | 924,672 | 25.0 | 5.0 | 3.0 | Fail | Pass | Fail | Fail |
| 55 | LCD | 15 | 924,672 | 35.0 | 5.0 | 3.0 | Fail | Fail | Fail | Fail |
| 56 | LCD | 15 | 924,672 | 20.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 57 | LCD | 15 | 924,672 | 37.0 | NR | NR | NI | Fail | NR | NR |
| 58 | LCD | 15 | 924,672 | 50.0 | NR | NR | NI | Fail | NR | NR |
| 59 | LCD | 15 | 924,672 | 32.0 | 3.0 | 1.0 | Fail | Fail | Fail | Pass |
| 60 | LCD | 15 | 924,672 | 20.0 | 3.0 | 1.0 | Fail | Pass | Fail | Pass |
| 61 | LCD | 15 | 924,672 | 36.0 | NR | NR | NI | Fail | NR | NR |
| 62 | LCD | 15 | 924,672 | 38.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 63 | LCD | 15 | 924,672 | 19.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 64 | LCD | 15 | 924,672 | 35.0 | 4.0 | 2.0 | Fail | Fail | Fail | Fail |
| 65 | LCD | 15 | 924,672 | 20.0 | 2.0 | 1.0 | Pass | Pass | Pass | Pass |
| 66 | LCD | 15 | 924,672 | 22.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 67 | LCD | 15 | 924,672 | 20.0 | 2.0 | 1.0 | Pass | Pass | Pass | Pass |
| 68 | LCD | 15 | 924,672 | 35.0 | 2.0 | 2.0 | Fail | Fail | Pass | Fail |
| 69 | LCD | 15 | 924,672 | 23.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 70 | LCD | 15 | 924,672 | 34.0 | NR | NR | NI | Fail | NR | NR |
| 71 | LCD | 15 | 924,672 | 34.0 | NR | NR | NI | Fail | NR | NR |
| 72 | LCD | 17 | 1,310,720 | 54.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 73 | LCD | 17 | 1,310,720 | 54.0 | NR | NR | NI | Fail | NR | NR |
| 74 | LCD | 17 | 1,310,720 | 41.0 | 4.0 | 4.0 | NI | Fail | Fail | Fail |
| 75 | LCD | 17 | 1,310,720 | 41.0 | NR | NR | NI | Fail | NR | NR |
| 76 | LCD | 17 | 1,310,720 | 38.0 | NR | NR | NI | Fail | NR | NR |
| 77 | LCD | 17 | 1,310,720 | 40.0 | NR | NR | NI | Fail | NR | NR |
| 78 | LCD | 17 | 1,310,720 | 48.0 | NR | NR | NI | Fail | NR | NR |
| 79 | LCD | 17 | 1,310,720 | 38.0 | NR | NR | NI | Fail | NR | NR |
| 80 | LCD | 17 | 1,310,720 | 38.0 | 2.0 | 2.0 | Fail | Fail | Pass | Fail |
| 81 | LCD | 17 | 1,310,720 | 45.0 | 2.0 | 2.0 | Fail | Fail | Pass | Fail |
| 82 | LCD | 17 | 1,310,720 | 35.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 83 | LCD | 18 | 1,310,720 | 55.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 84 | LCD | 18 | 1,310,720 | 67.0 | 5.0 | 5.0 | Fail | Fail | Fail | Fail |
| 85 | LCD | 18 | 1,310,720 | 54.0 | 5.0 | 5.0 | Fail | Fail | Fail | Fail |
| 86 | LCD | 18 | 1,310,720 | 49.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 87 | LCD | 18 | 1,310,720 | 65.0 | NR | NR | NI | Fail | NR | NR |
| 88 | LCD | 18 | 1,310,720 | 45.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 89 | LCD | 18 | 1,310,720 | 65.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 90 | LCD | 18 | 1,310,720 | 53.0 | 3.0 | 2.0 | Fail | Fail | Fail | Fail |
| 91 | LCD | 18 | 1,310,720 | 38.0 | 3.0 | 1.0 | Fail | Fail | Fail | Pass |
| 92 | LCD | 18 | 1,310,720 | 50.0 | 1.0 | 1.0 | Fail | Fail | Pass | Pass |

Notes: 1) NR = Not Reported; and 2) $\mathrm{NI}=$ Not Included in analysis due to all appropriate values not being reported. 3) Total Number of LCDs has decreased by 6 because wide-screen models are not included in the overall analysis for Tier 2.

| Model No. | Display Type | Screen Size | Total Pixels | Avg. On | Avg. Sleep | Avg. Off | All Modes Tier 2 | On Tier 2 | Sleep Tier 2 | $\begin{gathered} \hline \text { Off - } \\ \text { Tier } 2 \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 93 | LCD | 19 | 1,310,720 | 46.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 94 | LCD | 19 | 1,310,720 | 60.0 | 3.0 | 1.0 | Fail | Fail | Fail | Pass |
| 95 | LCD | 19 | 1,310,720 | 40.0 | 2.0 | 1.0 | Fail | Fail | Pass | Pass |
| 96 | LCD | 19 | 1,310,720 | 40.0 | 2.0 | 1.0 | Fail | Fail | Pass | Pass |
| 97 | LCD | 12 | 924,672 | 33.0 | NR | NR | NI | Fail | NR | NR |
| 98 | LCD | 20 | 1,310,720 | 85.0 | NR | NR | NI | Fail | NR | NR |
| 99 | LCD | 20 | 1,310,720 | 77.0 | 8.0 | 8.0 | Fail | Fail | Fail | Fail |
| 100 | LCD | 20 | 1,310,720 | 63.0 | 4.0 | 4.0 | Fail | Fail | Fail | Fail |
| 101 | LCD | 20.1 | 1,920,000 | 54.0 | 1.0 | 1.0 | Pass | Pass | Pass | Pass |
| 102 | LCD | 21 | 1,920,000 | 90.0 | 5.0 | 5.0 | Fail | Fail | Fail | Fail |
| 103 | LCD | 13 | 1,310,720 | 40.0 | NR | NR | NI | Fail | NR | NR |
| 104 | LCD | 14 | 924,672 | 40.0 | NR | NR | NI | Fail | NR | NR |
| 105 | LCD | 14 | 924,672 | 40.0 | NR | NR | NI | Fail | NR | NR |
| 106 | LCD | 15 | 924,672 | 24.0 | 3.0 | 3.0 | Fail | Pass | Fail | Fail |
| 107 | LCD | 15 | 924,672 | 29.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 108 | LCD | 15 | 924,672 | 23.0 | 2.0 | 2.0 | Fail | Pass | Pass | Fail |
| 109 | LCD | 17 | 1,310,720 | 39.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 110 | LCD | 17 | 1,310,720 | 44.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 111 | LCD | 17 | 1,310,720 | 38.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 112 | LCD | 19 | 1,310,720 | 46.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 113 | LCD | 19 | 1,310,720 | 46.0 | 3.0 | 3.0 | Fail | Fail | Fail | Fail |
| 114 | LCD | 17 | 1,310,720 | 29.7 | 2.1 | 2.1 | Fail | Pass | Fail | Fail |
| 115 | LCD | 19 | 1,310,720 | 38.9 | 4.4 | 4.4 | Fail | Fail | Fail | Fail |
| 116 | LCD | 15 | 924,672 | 37.0 | 3.0 | 1.0 | Fail | Fail | Fail | Pass |
| 117 | LCD | 15 | 924,672 | 35.0 | 3.0 | 2.0 | Fail | Fail | Fail | Fail |
| 118 | LCD | 17 | 1,310,720 | 45.0 | 2.0 | 1.0 | Fail | Fail | Pass | Pass |
| 119 | LCD | 18 | 1,310,720 | 53.0 | 3.0 | 2.0 | Fail | Fail | Fail | Fail |
| 120 | LCD | 18 | 1,310,720 | 43.0 | 3.0 | 1.0 | Fail | Fail | Fail | Pass |
| 121 | LCD | 15 | 786,432 | 21.0 | 3.7 | 0.4 | Fail | Pass | Fail | Pass |
| 122 | LCD | 15 | 786,432 | 18.1 | 0.7 | 0.6 | Pass | Pass | Pass | Pass |
| 123 | LCD | 17 | 1,310,720 | 31.4 | 0.7 | 0.7 | Pass | Pass | Pass | Pass |
| 124 | LCD | 19 | 1,310,720 | 33.1 | 0.7 | 0.6 | Pass | Pass | Pass | Pass |
| 125 | LCD | 15 | 786,432 | 19.8 | 2.6 | 1.5 | Fail | Pass | Fail | Fail |
| 126 | LCD | 15 | 786,432 | 19.9 | 1.5 | 1.5 | Fail | Pass | Pass | Fail |
| 127 | LCD | 15 | 786,432 | 19.4 | 0.5 | 0.4 | Pass | Pass | Pass | Pass |
| 128 | LCD | 17 | 1,310,720 | 35.1 | 1.8 | 0.8 | Pass | Pass | Pass | Pass |
| 129 | LCD | 18.1 | 1,310,720 | 49.1 | 2.0 | 1.1 | Fail | Fail | Pass | Fail |
| 130 | LCD | 19.1 | 1,310,720 | 32.1 | 0.8 | 0.7 | Pass | Pass | Pass | Pass |
| 131 | LCD | 20.1 | 1,920,000 | 61.5 | 0.1 | 0.1 | Fail | Fail | Pass | Pass |
| 132 | LCD | 17 | 1,310,720 | 38.6 | 2.6 | 0.6 | Fail | Fail | Fail | Pass |
| 133 | LCD | 17.1 | 1,310,720 | 22.2 | 0.9 | 0.7 | Pass | Pass | Pass | Pass |
| 134 | LCD | 15 | 786,432 | 23.4 | 1.0 | 0.8 | Pass | Pass | Pass | Pass |
| 135 | LCD | 17 | 1,310,720 | 27.2 | 0.9 | 0.9 | Pass | Pass | Pass | Pass |
| 136 | LCD | 19 | 1,310,720 | 26.8 | 1.0 | 0.9 | Pass | Pass | Pass | Pass |
| 137 | LCD | 19 | 1,310,720 | 29.0 | 1.0 | 0.9 | Pass | Pass | Pass | Pass |

Notes: 1) NR = Not Reported; and 2) $\mathrm{NI}=\mathrm{Not}$ Included in analysis due to all appropriate values not being reported. 3) Total Number of LCDs has decreased by 6 because wide-screen models are not included in the overall analysis for Tier 2.

