



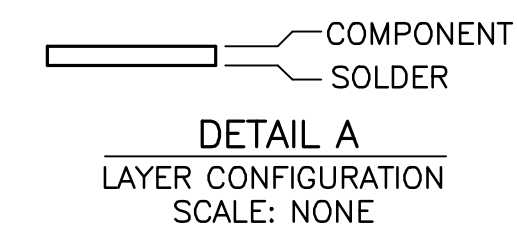













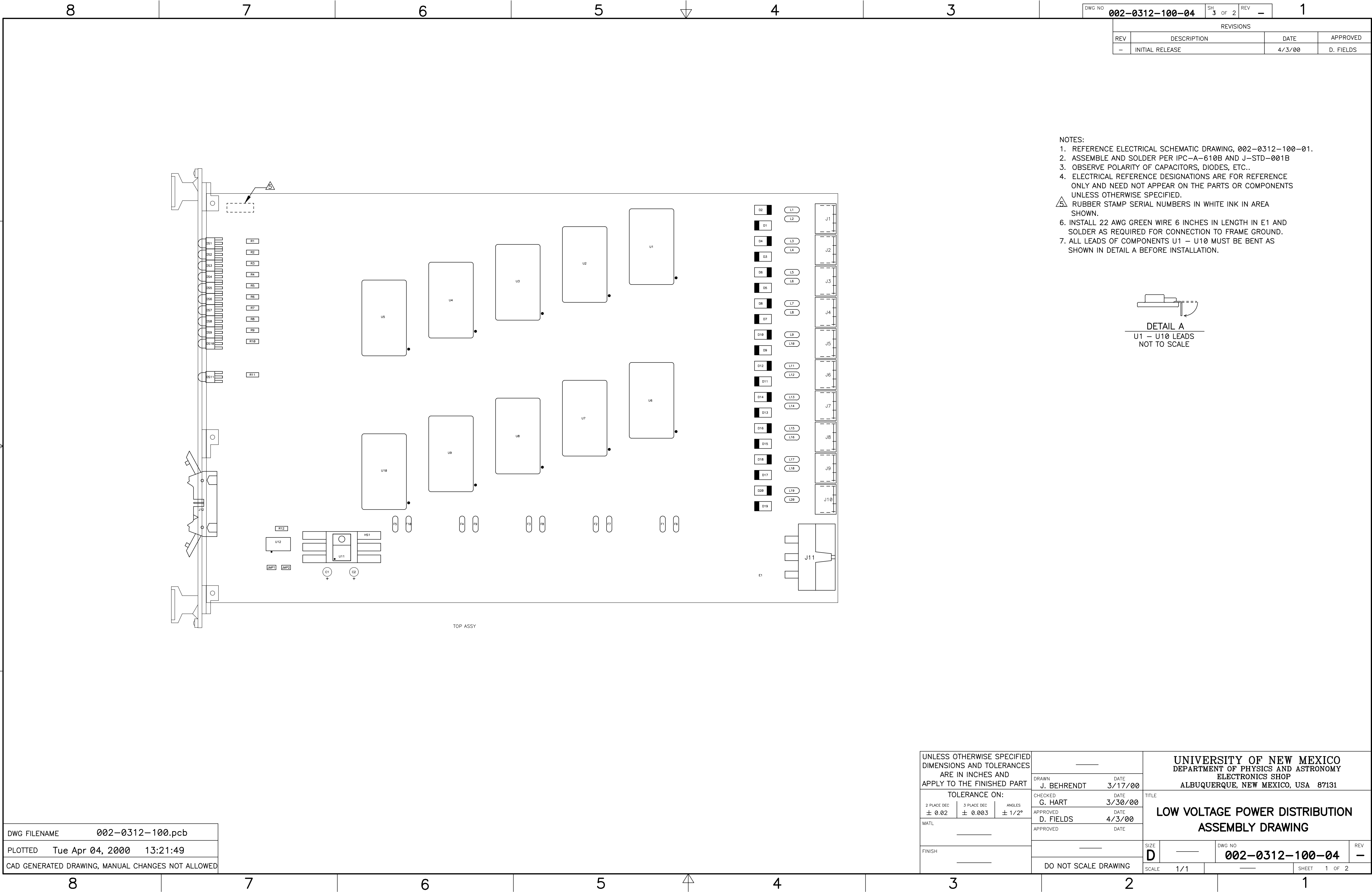
1.  BASE MATERIAL SHALL BE LAMINATED GLASS EPOXY, NEMA GRADE FR-4 (SHALL MEET UL 94V-0).
2.  FINISHED BOARD THICKNESS TO BE .062 +/- .005" EXCLUDING COPPER FOIL.
3.  TRACE LAYERS TO BE 2 OZ. COPPER.
4. FABRICATE BOARDS IN ACCORDANCE WITH IPC-6011 AND IPC-6012, CLASS 2.
5.  PCB SHALL BE IDENTIFIED WITH MANUFACTURERS APPROVED LOGO AND TYPE DESIGNATION IN ETCH IN AREA SHOWN.
6. SOLDERMASK OVER BARE COPPER, USING WET FILM, GREEN.
7. SILKSCREEN COMPONENT SIDE USING NON-CONDUCTIVE WHITE EPOXY INK.
8. CONSTRUCTION OF BOARD AS SHOWN IN DETAIL "A".
9. ALL HOLE SIZES SPECIFIED ARE AFTER PLATING. ANY HOLES NOT PLATED THRU ARE SPECIFIED IN THE DRILL SYMBOL TABLE.
10. FOR ARTWORK SEE DWG NO. 002-0312-100-03.
11. BOARD EDGES (TOP & BOTTOM) MUST REMAIN CLEAR OF SOLDER MASK.



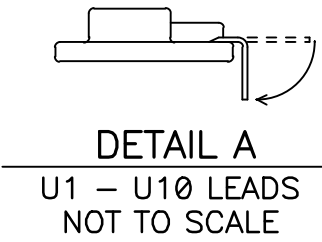
Hole Dia (inch)	Symbol	Quantity	Plated
0.028		37	Yes
0.038		32	Yes
0.042		80	Yes
0.046		20	Yes
0.055		74	Yes
0.067		40	Yes
0.106		4	No
0.110		6	Yes
0.116		2	No
0.120		2	No
0.125		1	No

TOTAL PLATED HOLES (FILE: 0002T01.NC1)	271
TOTAL UNPLATED HOLES (FILE: 0002T01.NC2)	9

UNLESS OTHERWISE SPECIFIED DIMENSIONS AND TOLERANCES ARE IN INCHES AND APPLY TO THE FINISHED PART			_____		UNIVERSITY OF NEW MEXICO DEPARTMENT OF PHYSICS AND ASTRONOMY ELECTRONICS SHOP ALBUQUERQUE, NEW MEXICO, USA 87131							
			DRAWN J. BEHRENDT		DATE 3/17/00		TITLE LOW VOLTAGE POWER DISTRIBUTION FABRICATION DRAWING					
			CHECKED G. HART		DATE 3/30/00							
			APPROVED D. FIELDS		DATE 4/3/00							
TOLERANCE ON:			APPROVED _____		DATE _____		SIZE D _____		DWG NO 002-0312-100-02		REV —	
2 PLACE DEC ± 0.02		3 PLACE DEC ± 0.003		ANGLES ± 1/2°		SCALE 1/1 _____			SHEET 1 OF 1			
MAT'L			_____		DO NOT SCALE DRAWING							
FINISH _____			_____		DO NOT SCALE DRAWING							
_____			_____		_____		_____		_____		_____	

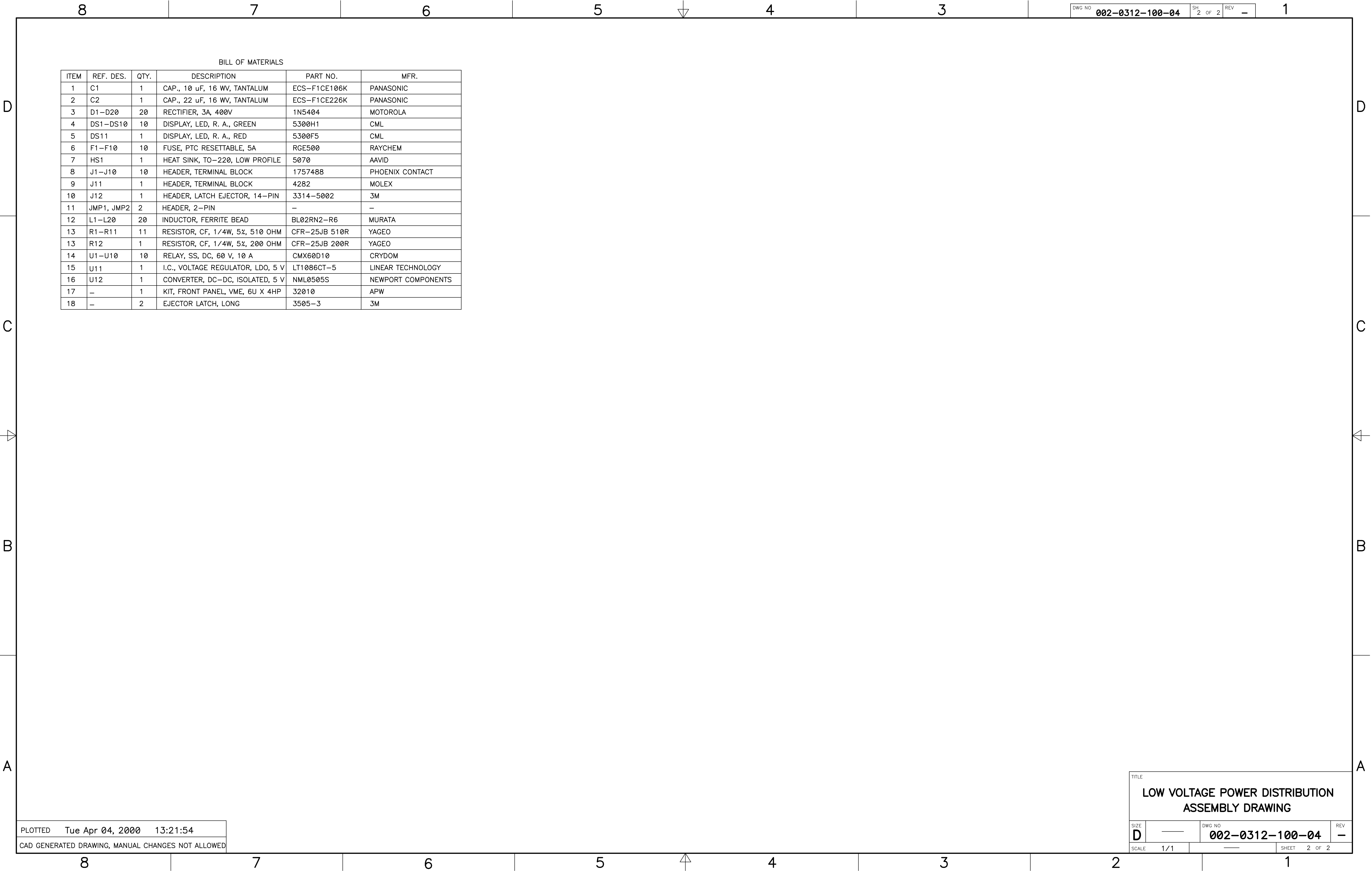


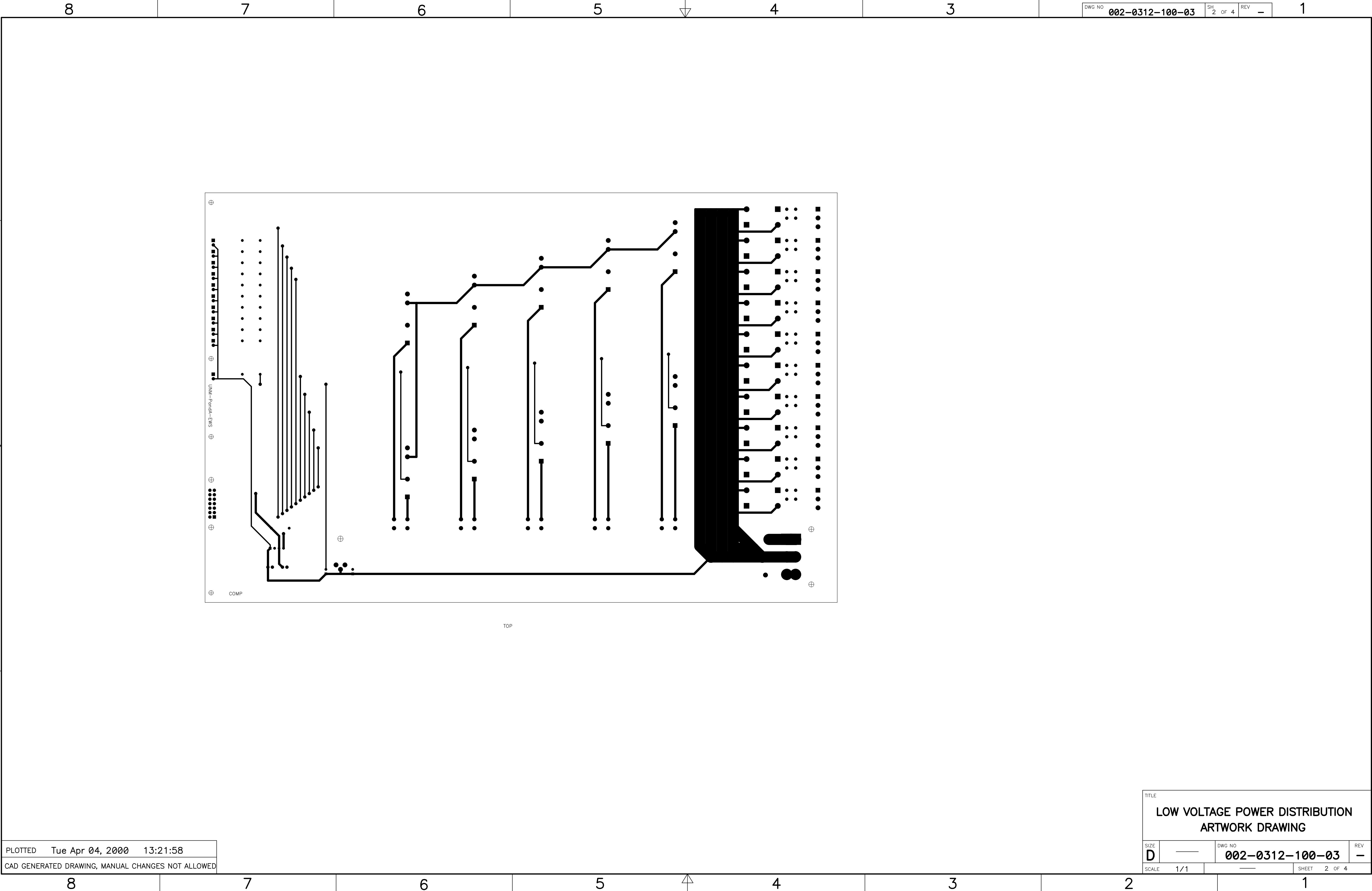
- NOTES:
1. REFERENCE ELECTRICAL SCHEMATIC DRAWING, 002-0312-100-01.
 2. ASSEMBLE AND SOLDER PER IPC-A-610B AND J-STD-001B
 3. OBSERVE POLARITY OF CAPACITORS, DIODES, ETC..
 4. ELECTRICAL REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY AND NEED NOT APPEAR ON THE PARTS OR COMPONENTS UNLESS OTHERWISE SPECIFIED.
 5. RUBBER STAMP SERIAL NUMBERS IN WHITE INK IN AREA SHOWN.
 6. INSTALL 22 AWG GREEN WIRE 6 INCHES IN LENGTH IN E1 AND SOLDER AS REQUIRED FOR CONNECTION TO FRAME GROUND.
 7. ALL LEADS OF COMPONENTS U1 - U10 MUST BE BENT AS SHOWN IN DETAIL A BEFORE INSTALLATION.



DWG FILENAME	002-0312-100.pcb
PLOTTED	Tue Apr 04, 2000 13:21:49
CAD GENERATED DRAWING, MANUAL CHANGES NOT ALLOWED	

UNLESS OTHERWISE SPECIFIED DIMENSIONS AND TOLERANCES ARE IN INCHES AND APPLY TO THE FINISHED PART			DRAWN J. BEHRENDT		DATE 3/17/00	UNIVERSITY OF NEW MEXICO DEPARTMENT OF PHYSICS AND ASTRONOMY ELECTRONICS SHOP ALBUQUERQUE, NEW MEXICO, USA 87131		
			CHECKED G. HART		DATE 3/30/00	TITLE LOW VOLTAGE POWER DISTRIBUTION ASSEMBLY DRAWING		
TOLERANCE ON:			APPROVED D. FIELDS		DATE 4/3/00	SIZE D		
2 PLACE DEC ± 0.02			3 PLACE DEC ± 0.003		ANGLES ± 1/2°	DWG NO 002-0312-100-04		
MATL			APPROVED		DATE	REV -		
FINISH			DO NOT SCALE DRAWING		SCALE 1/1	SHEET 1 OF 2		





D

D

C

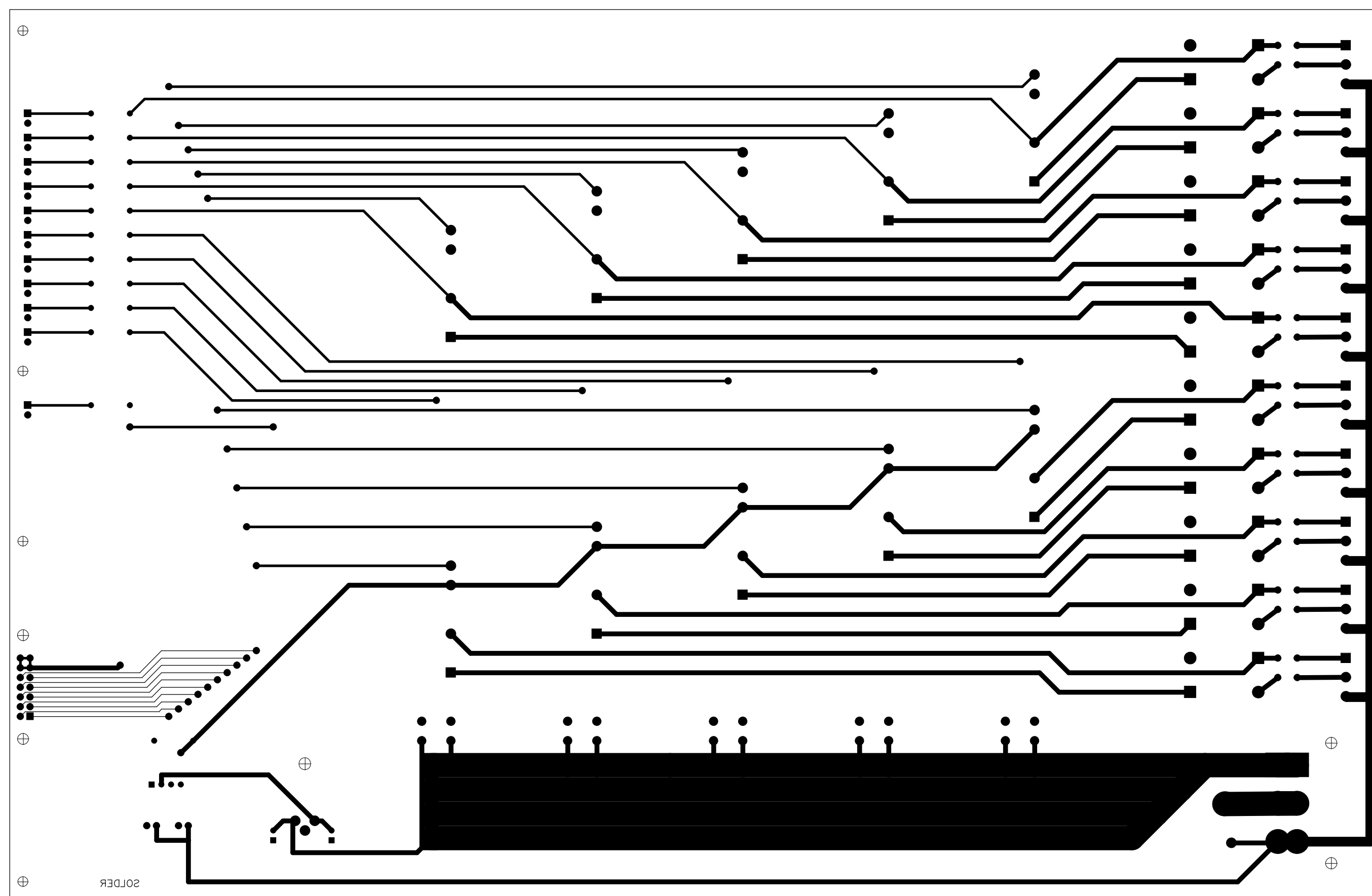
C

B

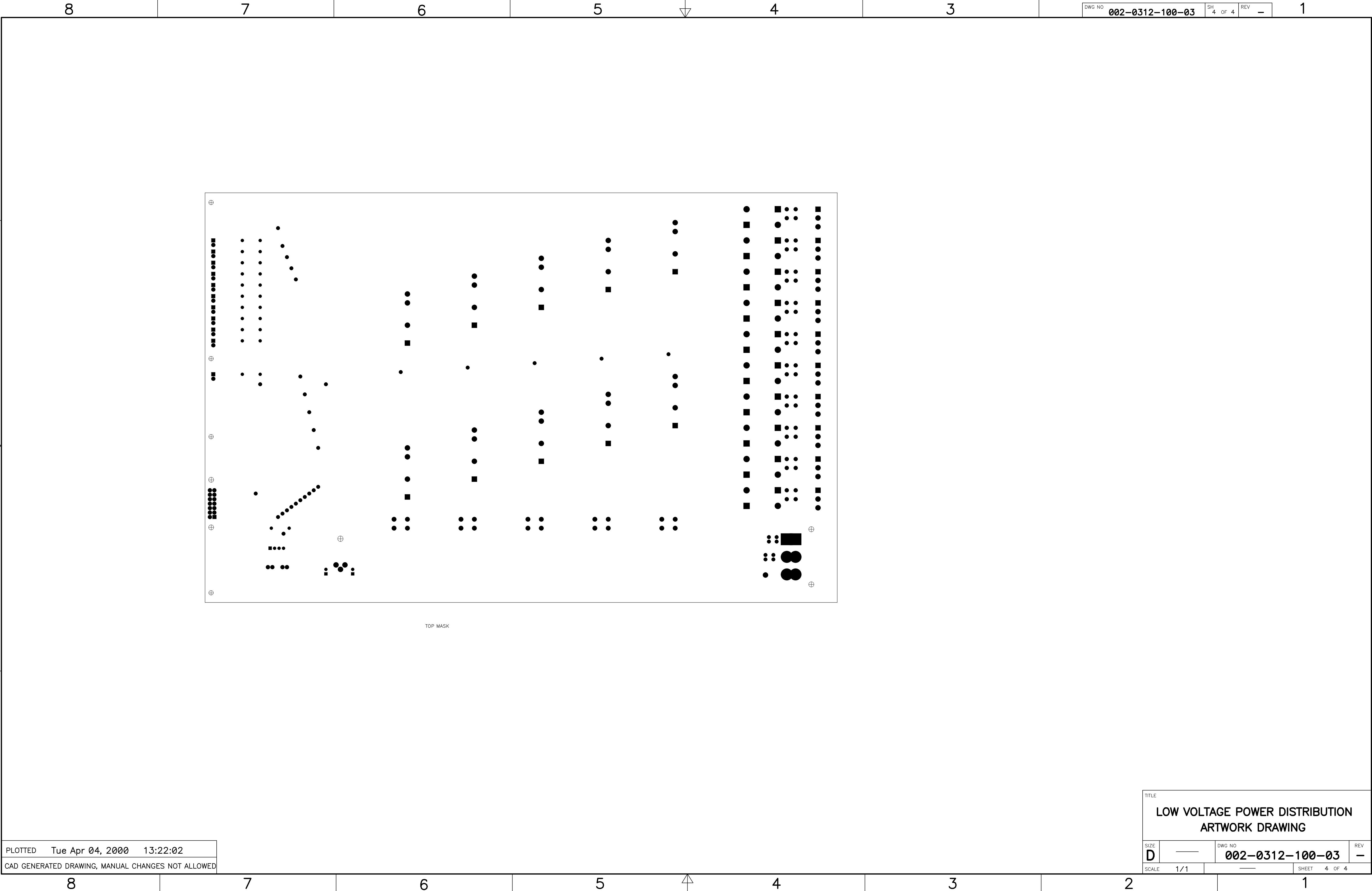
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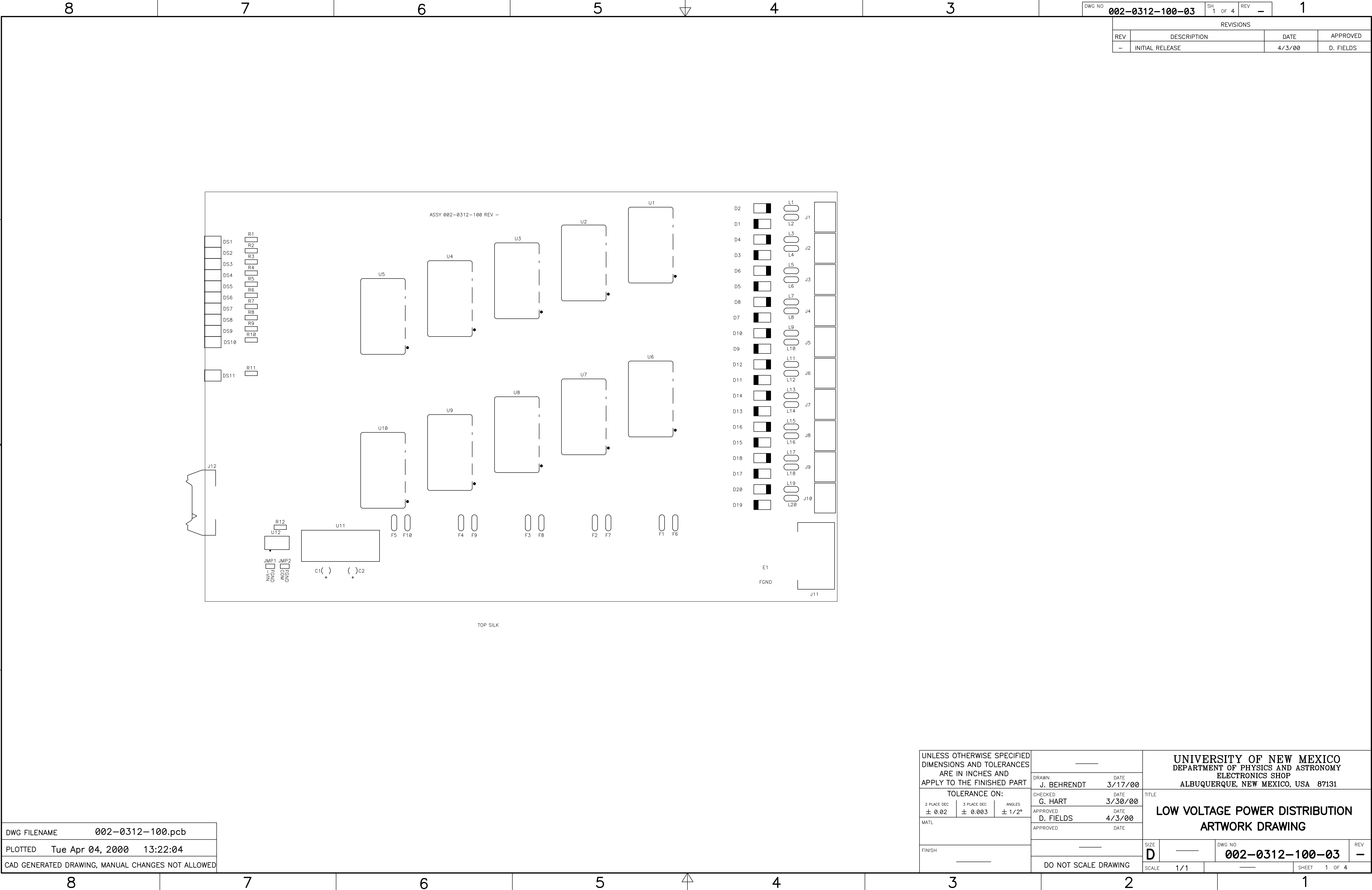
A

A



BOTTOM





DWG FILENAME

002-0312-100.pcb

PLOTTED

Tue Apr 04, 2000 13:22:04

CAD GENERATED DRAWING, MANUAL CHANGES NOT ALLOWED

UNLESS OTHERWISE SPECIFIED
DIMENSIONS AND TOLERANCES
ARE IN INCHES AND
APPLY TO THE FINISHED PART

TOLERANCE ON:

2 PLACE DEC

3 PLACE DEC

ANGLES

± 0.02

± 0.003

$\pm 1/2^\circ$

MATL

FINISH

DRAWN

J. BEHRENDT

DATE

3/17/00

CHECKED

G. HART

DATE

3/30/00

APPROVED

D. FIELDS

DATE

4/3/00

APPROVED

DATE

UNIVERSITY OF NEW MEXICO
DEPARTMENT OF PHYSICS AND ASTRONOMY
ELECTRONICS SHOP
ALBUQUERQUE, NEW MEXICO, USA 87131

TITLE

LOW VOLTAGE POWER DISTRIBUTION
ARTWORK DRAWING

SIZE

D

SCALE

1/1

DWG NO

002-0312-100-03

REV

-

SHEET

1 OF 4

