Dirc Online Status Report

- DAQ
  - Complete and Final Dirc electronics now in US
    - Electronics-only Setup at SLAC used for dataflow core development
    - Testbench at LBL with PMTs to develope dirc-specific software
      - ROM + FCTS will come to LBL in May
  - Dirc working closely with Dataflow group at SLAC
    - Following rapid core software changes
    - Dirc software prototypes provide feedback to dataflow
  - Front-end electronics configuration
    - DFB registers can be loaded from a data file
  - Feature extraction run on L1 accept transition in ROM
  - Full DAQ chain demonstrated DFB -> OEP (4/29/98)
    - Raw data pre-loaded into front-end electronics buffers
- Calibration
  - Dataflow can now generate calibration strobe (5/2/98)
  - Example OEP electronics calibration module
  - Example Prompt Reconstruction datastream module
    - Refinement of T0 using collision data
    - Tested with simulated data
  - Simulated calibration data stored in Conditions Database
    - Used in reconstruction algorithms for T0, status.
  - Algorithm development in progress
- DetectorControls
  - 1 HV control mainframe now installed + working at LBL
  - Integration of DIRC DC with online begun
  - Integration of DC with DAQ at LBL testand in June

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