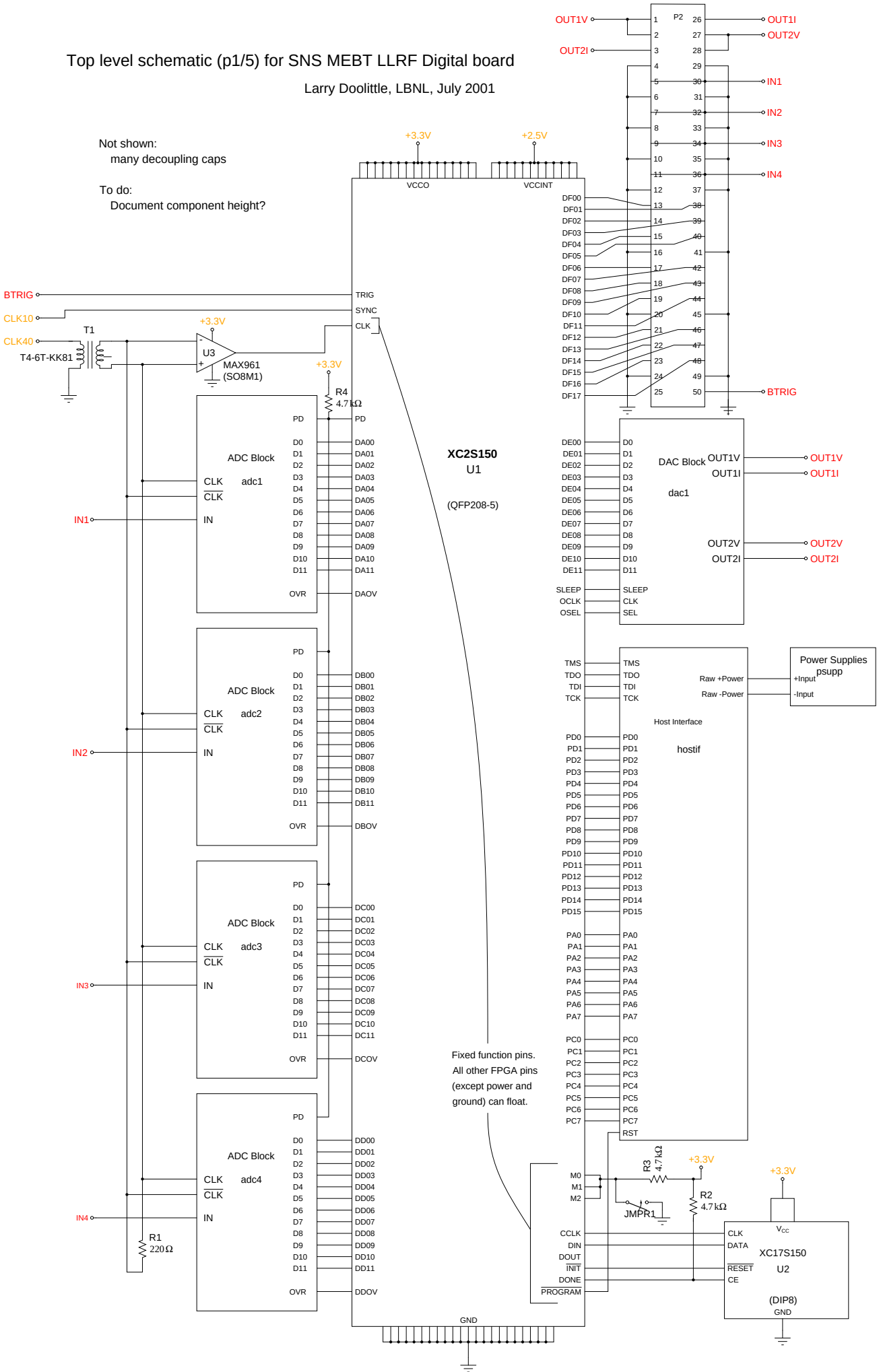
Top level schematic (p1/5) for SNS MEBT LLRF Digital board

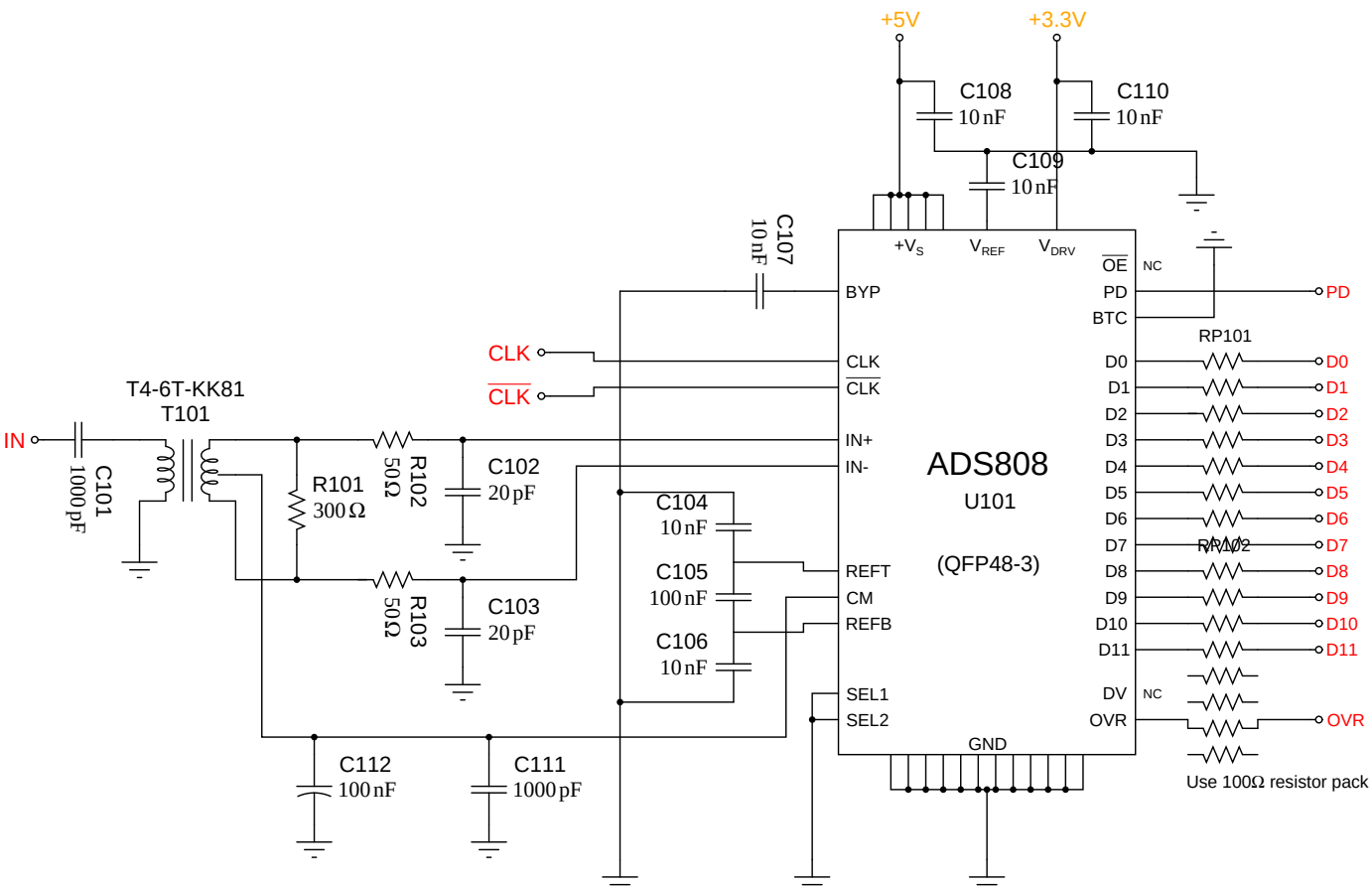
Larry Doolittle, LBNL, July 2001

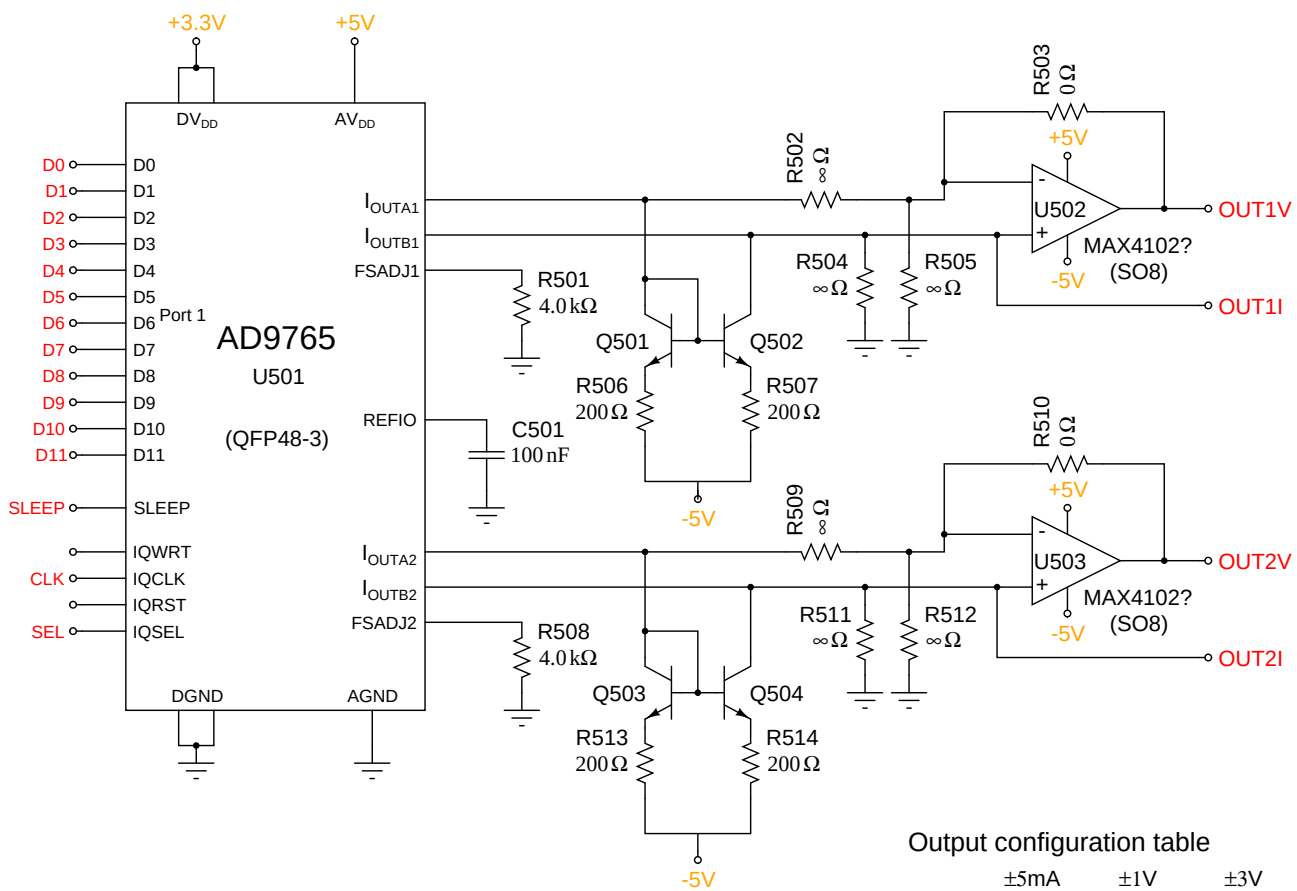
Not shown:
many decoupling caps

To do:
Document component height?



4 instances: 1xx part IDs are replicated as 2xx, 3xx, and 4xx





Output configuration table

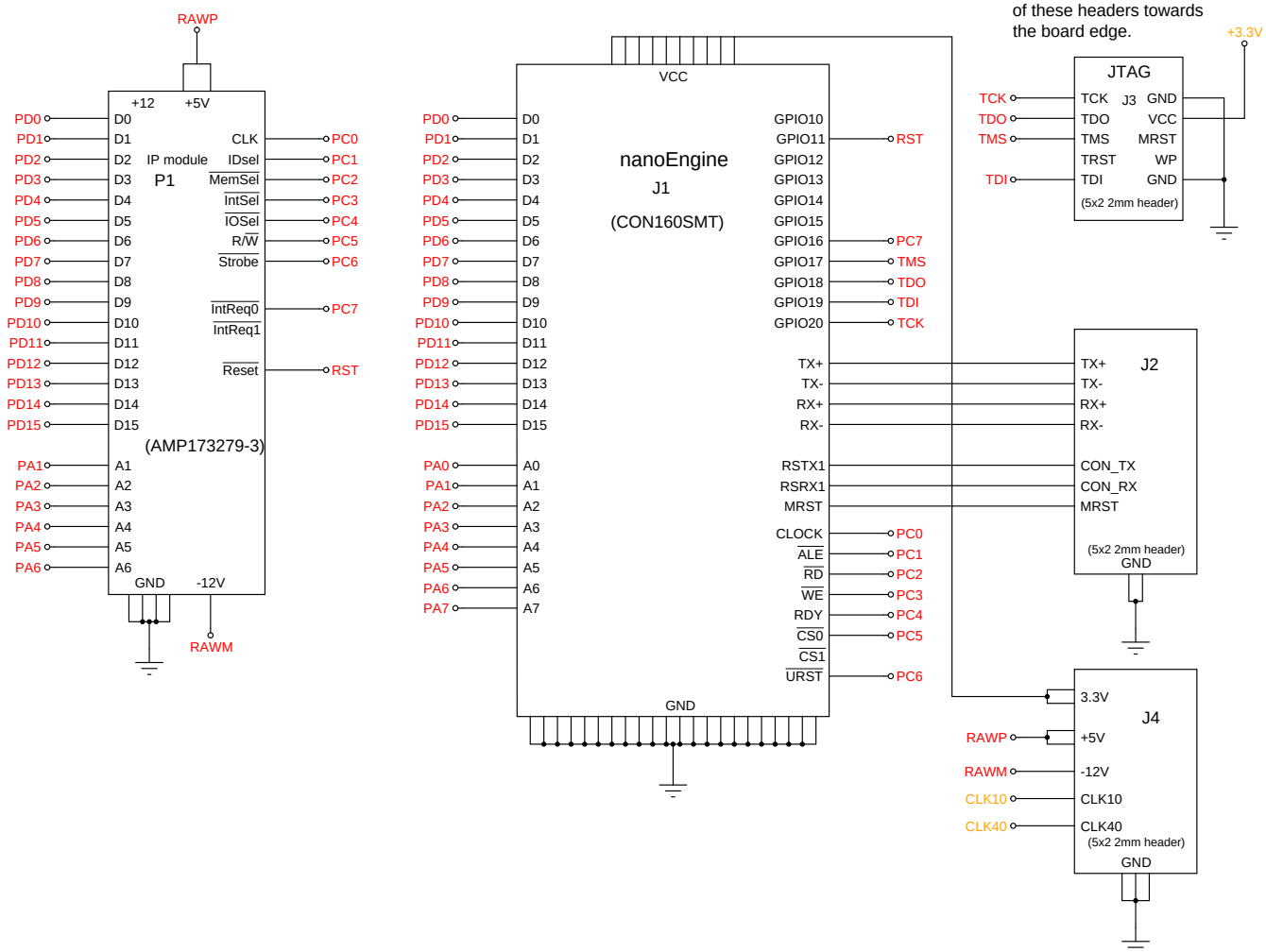
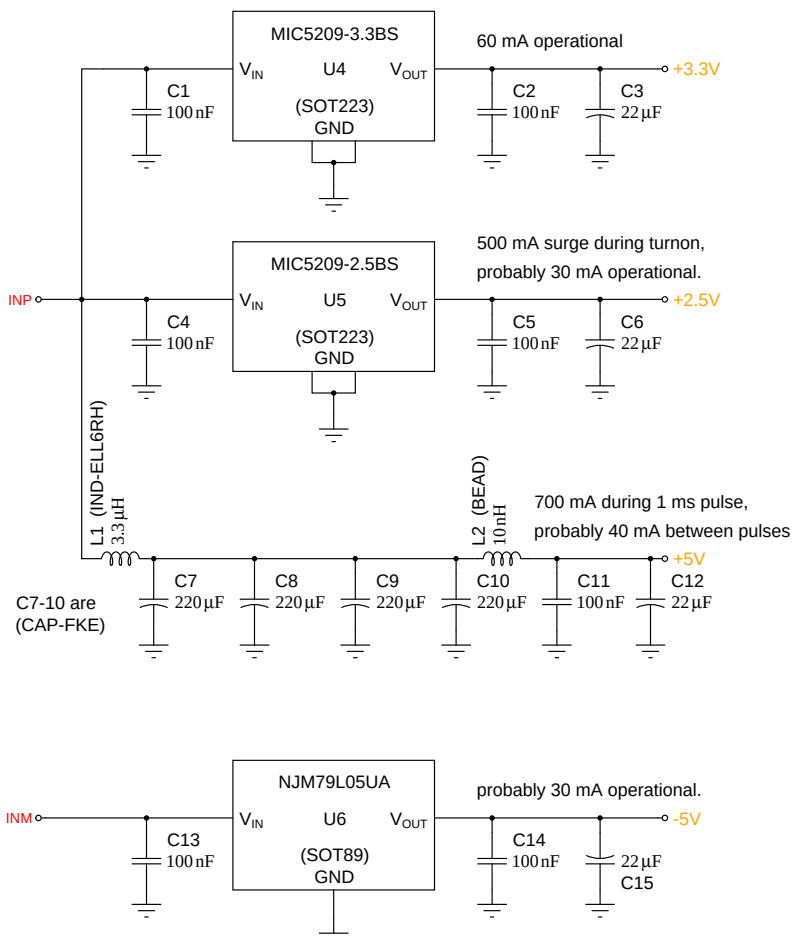
		±5mA	±1V	±3V
R502	R509	∞ Ω	0 Ω	∞ Ω
R503	R510	0 Ω	200 Ω	2 kΩ
R504	R511	∞ Ω	200 Ω	200 Ω
R505	R512	∞ Ω	∞ Ω	1 kΩ
Q501-504		√		√

Host Interface

Note: IP carrier is a source of power, the nanoEngine is a consumer of power

Only one host at a time is attached.

Face the 2,4,6,8,10 side of these headers towards the board edge.



Power Supplies