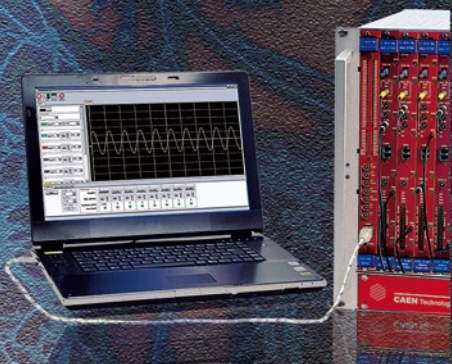


- 8/12/14 bit digitizers
- 4/8/64 channels
- Up to 1 GS/s
- External/internal Trigger
- Multi-Board synchronisation
- Zero Suppression and Data Reduction algorithms
- Memory buffer: up to 10 MSample/ch, up to 1024 events
- Optical Link interface
- VME64X compliant
- Firmware upgradeable via VME/Optical Link



- Direct PC control of up to 8 digitizers using A2818 PCI/Optical link card
- Libraries, Demos (C and LabView) and Software tools for Windows and Linux



Small details 
Great differences

NEW

Model	#ch	Res (bit)	Dynamic Range (Vpp)	Sample Rate (MS/s)	Bandwidth (MHz)	Memory (Sample/ch)	Max Recorded time (ms)
V1724	8	14	2.25 - 10	100	DC to 40	512K / 4M	~5 / 42
V1740	64	12	2-10	65	DC to 30	160K / 1.28M	~3 / 21
V1720	8	12	2	250	DC to 125	1.25M / 10M	~5 / 42
V1721	8	8	1	500	DC to 250	2M	~4
V1731	8 - 4	8	1	500 - 1000	DC to 250 - 500	2M - 4M	~4