Module: L1_Hardware_Interface

Functions:

```
int L1_Hardware_Interface ()
static int num_message()
STATUS fillEventDesc();
static STATUS sciDataSend(int* dest_addr, int* src_address,
int size);
```

Description:
Task which interfaces with the TCU board. Reads FIFO status to see if the FIFOs are not empty. If this is true, information FIFO 1 is read and the token is determined. Then the other two FIFOs are read and a data unpacked into the event descriptor. A message is sent to L1Control informing it about availability of data to be read from DSM boards.

Interfaces:

`int L1_Token_Manager ()` sends tokens for loading into TCU.
`STATUS L1Control()` receives message to send data to analysis CPU.