Heavy Quark Detection with a Forward Silicon Micro-vertex Detector at PHENIX

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Project Overview

- A new state of matter, the quark-gluon plasma (QGP), is being probed in collisions of heavy ions at RHIC.
- Heavy quarks (charm and beauty) are the cleanest probe of QGP. Next frontier of QGP physics. Want to measure their energy loss and flow in QGP.
- We will construct a forward silicon micro-vertex detector → unique heavy quark experimental capability at RHIC (|y| > 0).
- Close collaboration between theory, simulation and experiment.
- Quantitative determination of QGP properties.

The PHENIX Muon Arms



Muon Arms were designed for heavy quark measurements!

Forward Micro-Vertex Detector





- 2 Sectors of 4 Tracking stations composed of Si pixels, 50 by 400 um
- Cover ~ 1/8 of one muon arm (one octant) with 200 detector modules
- Electronics recently developed by FNAL for BTeV, ~4M pixels. Low power, high speed and high resolution pixel detector.
- Can detect large numbers of D and some B decays per year at RHIC.

BTeV Pixel Detector Module

- Pixel Sensor bump-bonded to Readout chip
- Fine segmentation
 - $50 \ \mu m \ x \ 400 \ \mu m$
 - Large number of channels
 - Electronics in the active tracking volume
 - High power density
 → cooling system required
- Basic building block Multichip Module (MCM)
 - 8 readout chips / module
 - HDI and flex cables
- Assemble modules on both sides of substrate to form pixel plane; providing a high resolution radius measurement plus a good phi measurement.



Multichip module





BTeV Ladder Design



We will build ladders containing only Y-view modules. Zero degree C liquid cooling instead of LN_2 .

Readout Electronics Chain

- Silicon Detector
 - Array of 50 x 400 micron pixels
 - Bump bonded to 8 front end chips
- Front End Chip
 - FPIX2 with data push, LVDS output, 840 Mb/sec
 - Wire bonded to high density interconnect, 10m cable with digital output and control lines
- Receiver / Controller Board
 - Xilinx FPGA emulates PHENIX readout standard
 - Interface to PHENIX fast and slow controls
 - Possible Level I trigger
- Optical Fiber Interface and Cable to Counting House
- **PHENIX Data Collection Module**

Heavy Quark Yields*, for 1/8 of a Muon Arm

	Run	Ions	Luminos. on tape	$D \rightarrow \mu$ triggered counts	$B \rightarrow \mu$ triggered counts	$B \rightarrow J/\psi \rightarrow \mu$ with 1 μ in SiVTX	
	2007	p+p	67 pb ⁻¹	28*106	24*10 ³	240	
	2008	Au+Au	760 ub ⁻¹	8*10 ⁶	6*10 ³	160	
• Rat	tes before tex cut.	applicatio Z-Ve Resol	n of a ertex lution, n	500 450 400 350 300 250 200	strip width = 025 µ r strip width = 050 µ r strip width = 075 µ r strip width = 100 µ r	m m	

150

100

Muon Momentum, GeV

11

Dramatic Signal / Background Improvement for Heavy Quarks



Hardware Cost and Schedule

Silicon	Front	Wire	Flex	Fiber	Receiver	Power	Support	Total
Sensors	End	Bonding	Cables,	Optics		Supplies	Structure	+
	Chips	Bumping	Hybrids			+ Cables		30%
\$130 K	\$90 K	\$75 K	\$15 ² K	\$25 K	\$40 K	\$30K	\$60 K	\$785 K

Cost estimates for SVD Hardware covering ~1/8 of a PHENIX Muon Arm Labor not included, expect similar labor and hardware costs.

- FY06 : Complete readout chip production with FNAL Procure Si detectors and bump-bond to readout chips Design and procure readout bus (HDI) Design support structure and cooling Design FPGA board
- FY07 : Assemble and test detector modules, mount on substrate Construct FPGA board, cabling, power and cooling Install 4 layer vertex detector in PHENIX
- FY08 : Record p+p or Au+Au data for charm decays

Theory Program

QCD Theory of Plasma (E. Mottola and I. Vitev) :

- Calculate transport coefficients viscosity, conductivity + diffusion
- Study approach to thermalization and equilibration
- Determine collective degrees of freedom and equation of state
- Calculate energy loss of heavy quarks due to gluon radiation, suppression of high p_T charm and beauty in QGP

Lattice QCD (R. Gupta) :

- Study Debye screening effects leading to dissociation of heavy quark bound states versus temperature (e.g. J/ψ yield versus T)
- Calculate equation of state versus temperature for QGP
- Lattice is only non-perturbative way to address QCD equilibrium properties

Participants + Support

LANL, P. McGaughey, E. Mottola, et al. Fermilab, D. Christian, et al. Iowa State Univ., J. Lajoie, C. Ogolvie, et al. NMSU, S. Pate and V. Papavasiliou, et al. UNM, D. Fields, et al. BNL, PHENIX upgrade team Hytec, Inc. of Los Alamos Columbia Univ., B. Cole, et al. ? We're looking for additional collaborators!

LANL Budget : ~1.25 M\$/year for 3 years LANL project approved during summer '05. Funding began in FY06.

Summary

Definitive Physics of New State of Matter:

- Charm and beauty production cross sections
- Energy loss and flow of charm and beauty in QGP
- Measurement of QGP properties energy density, temperature, transport properties, viscosity, conductivity

State-of-the-Art Silicon Vertex Detector:

- Si end cap covering 1/8 muon arm (4 pixel layers)
- Leverage DOE funds for full detector (\$5M) DOE Proposal to be submitted in CY 2006.

Unique Opportunity for Convergence of:

• Theory, simulation and experiment of quark-gluon plasma formation and decay in the laboratory

Backup slides

Why Heavy Quarks?

- Have mainly qualitative evidence for QGP formation can make quantitative measurements with heavy quarks
- Heavy quarks (charm and beauty) produced early in the collision. Live long enough to sample the plasma
- Intrinsic large mass scale allows precise calculations
- Mass dependence of diffusion of heavy quarks determines plasma properties, e.g. viscosity and conductivity
- Yields of charm and beauty pairs compared to first principle lattice simulations determine the energy density and temperature
- Comparison between light and heavy quark suppression distinguishes between theoretical models of energy loss in the QGP

Heavy quarks can provide an <u>order of magnitude</u> better determination of the properties of the plasma!



FNAL Readout Chip Comparison

Chip	Noise	Ministrip	Readout	Trigger	Power	Geometry
All 50 µm	Threshold σ		type	possible	per chan	r-phi
spacing			speed			
SVX4	S/N -12/1	yes	Pipeline	no	2 mW	yes
128 ch			53 MHz			
FSSR	250 e	yes	Data push	yes	3 mW	yes
128 ch	440 e		840 Mb			
FPIX	220 e	no	Data Push	yes	90 μW	no
2816 ch	125 e		840 Mb			

FSSR and FPIX chip are good candidates for LDRD project

Signal = 24000 e for 300 µm Si Sensor.