

# L3 CPS Calibration Code

Yi Jiang  
University of Michigan

- To get the calibration values
- To extract CPS channel and translate hardware ID to strip ID
- To read these calibration values
- To Set up these calibration values

# I. To get the calibration values

On online machine:

```
>setup d0online
```

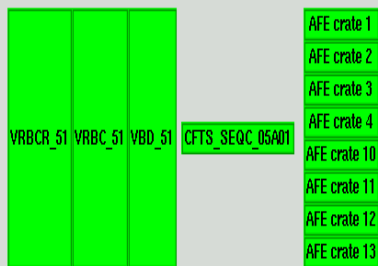
```
>cft_gui
```

(1) To get the calibration run number

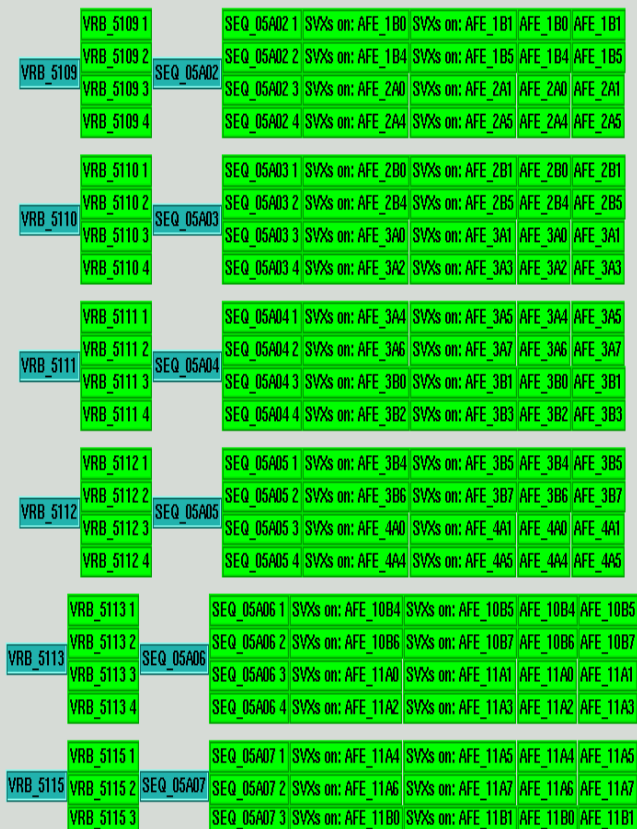
(2) To write pedestal values and threshold to a text file

The screenshot shows the cft\_gui interface. At the top, there is a menu bar with five buttons: "debug: off" (highlighted in green), "global parameter", "PIC temperature blue to green and more", "mystery clear", and "quit". Below the menu bar is a table with five rows, each representing a different calibration run. The first four rows are for VRBCR\_50, VRBCR\_51, VRBCR\_52, and VRBCR\_53. The fifth row is for VRBCR\_AFE\_TEST. Each row has a "download" button (highlighted in green) followed by a grid of ten buttons: "downloadNoSvxNoAfeNoVme", "reinit VME", "init VRBC", "on TmpCtl", "plot ThreshVrefVthres", "plot cryo", "plot bias", and "details".

Run ID	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details
VRBCR_50	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details
VRBCR_51	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details
VRBCR_52	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details
VRBCR_53	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details
VRBCR_AFE_TEST	download	downloadNoSvxNoAfeNoVme	reinit VME	init VRBC	on TmpCtl	plot ThreshVrefVthres	plot cryo	plot bias	details



Channel chain



new vref
new SVX thresh
plot cryo SP-PV
plot ThreshVrefVthres
plot VrefVthres
plot Bias Readback
plot SVX Thresh change
plot Vref change
plot Vthres change
write PedThr to file
write Afe Phys Id
clear bias_tmp_cor
increase SVX thresh by 5 counts
decrease SVX thresh by 5 counts
write LED fits
new bias
On Bias
Off Bias
new adc_pedestal_ramp
print adc_pedestal_ramp
download High disc gain: default
download Low disc gain
print bias corrections
print calib runs
commit to off-line
new vthres
SEQ power off
VRB reset
VRB j3 disable
VRB j3 enable
VRB j3 default
VRB init
VRB read reg
Monitor PIC bias On
Monitor PIC bias Off
intialize default Bias Dac Constants
update Bias Dac Constants
update pipeline
temperature control on
temperature control off
increase vref by 5
decrease vref by 5
increase disc thresh by 1 pe
decrease disc thresh by 1 pe
set max disc thresh
set min disc thresh
download AFEs
download without AFEs
download without SVXs and AFEs

## II. extract CPS channel and translate hardware ID to strip ID

(Thanks to Drew Alton.)

- (1) a mapping file applied for translating the hardware address to strip ID
- (2) a script file used to extract CPS channel and save strip ID, pedestal value and threshold to three text file (axial layer, u\_layer and v\_layer each) .

```
26070a  43.1 48 45  →  -1234  65.5  73
26070b  43.1 48 45  →   1197  63.3  73
```

### III. read these calibration values

Code: `cps_util/CPSUnpacker.cpp`

Method:

```
void CPSUnpacker::setup_calibration(int type, float adc_per_mev,  
float strpthrsh, float pedestal)
```

```
    Array:    gains[iz][ilay][istrip]  
             pedestals[iz][ilay][istrip]  
             mip_values[iz][ilay][istrip]  
             thresholds[iz][ilay][istrip]
```

Where "type" is a rcp parameter,

`type=0`, use the default calibration values for MC;

`type=1`, read the calibration values from text file;

`type=other value`, access offline database to get calibration values;

## IV. Set up these calibration values

Code: `l3fcpsunp/L3TCPSUnpack.cpp`

Method:

```
void L3TCPSUnpack::RunInit()
```

At the beginning of each run, it will set up calibration constants.