NAME OF DOCUMENT	FILENAMES	DESCRIPTION
MRM Overview L8825-9220-DD-00	mr_brd.doc mr_brd.sch mr_brdtk.sch mr_brdfb.sch mr_brda.lib mr_u47.abl mrm_brda.bom	Written description of GeBGO. Schematic of MRM (3 sheets) Archived Parts in Schematic PAL Program for U47 (Token Control) Bill of Material for MRM
VXI Interface FPGA L8825-9222-DB-00	vxi_mr.doc vxi_mr.sch vxi_mux.sch vxi_regs.sch hexinv1.sch vxi_out.sch vxi_in.sch vxi_mr.lib vxi_mr.mcs	Written description of VXI Interface FPGA Schematics of VXI Interface (6 pages total) Archived Parts in Schematic Hex code used to program serial PROM
Palser FPGA L8825-9221-DB-00	pulser.doc pulser.sch read.sch pulsefor.sch gateform.sch pulser.lib pulsere.mcs	Written description of the Pulser FPGA. Schematic of the Pulser FPGA. Archived Parts in Schematic. Hex Code used to program serial PROM.
MRM Overview Appendix A: Abel Progams for 22V10s.	mr_u41f.abl	PAL Program for MRM-U41 (Token Control)

MRM Overview Appendix B: Test and Utility Programs	vxi_error.c regtest.c ge_memtest.c pulser.c ma16d16.c wca16d16.c rca16d16.c	Test programs for GeBGO For a description see Appendix B of the Local Readout Module Spec.
MRM Overview Appendix C: PARTS LIST and DATA SHEETS FOR MRM	mr_brda.bom XC3042-70-PC84C XC1736A 22V10 AMD7968 IDT7133 IDT73201 DS3890 DS3892 MC10125 96F172 96F173 74LS00 74LS08 74S32 74S38 74LS123 74S140 74LS240 74HC244 74LS645	Bill of Materials Xilinx FPGA Xilinx Serial Prom PAL AMD Taxi Chip Dual Port Ram (2k x 16) Pipeline Registers Octal Trapezoidal Driver Octal TRI-STATE Receiver Quad MECL to TTL Quad Differential Driver Quad Differential Receiver Quad two input NAND Quad two input AND Quad two input OR Quad NAND Open-Collector drain Monostale Multivibrator Quad two input NAND dual 4 input NAND Octal Inverting Buffer Octal Buffer Octal Buffer Octal Bus Transceiver