CLOCK DISTRIBUTION IN SLR STATIONS

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1. Introduction

A design and analysis technique for distributing the 10 MHz clock signal in a SLR station is presented; the design is using noise reduction techniques [5] and an edge sharpening technique in order to improve the cycle-to-cycle jitter [6] of the 10 MHz sine wave which affects the ranging precision.

2. Design Implementation

Most of the SLR stations in the world measure with off-the-shelf time interval counters (like HP5370B from Hewlett Packard or SR620 from Stanford Research Systems) to measure the laser runtime relative to their 10 MHz source. They use the asynchronous event (like laser return) as a start to the time interval counter and the next edge of the 10 MHz source as a common stop.

The 10 MHz source has a very good long-term stability. But because of its sinusoidial shape, the short-term jitter can be improved by sharpening the risetime and falltime. By applying noise reduction techniques (like filtering) and edge sharpening, the performance in terms of cycle-to-cycle jitter has improved [6]. A high Q quartz filter with Q = 2,000 at 10 MHz centre frequency is reducing the noise bandwidth down to [4]:

$$(f_{U3dB} - f_{L3dB}) \cdot \frac{\pi}{2} = 7.854 \text{ okHz}$$

A good filter design for reducing noise bandwidth is described in [6]. The following block diagram shows the design of the clock distributor built for a SLR station. It has been in successful and continuous operation at a station since June 1998.



Figure 1: Block diagram of the clock distributor and timing measurement of the outputs

The rms jitter of 43 ps of the 10 MHz sine wave is the same measured value from the output of the cesium clock as well as from the output of the clock distributor device.

The key is to produce very fast output transitions ($t_r \approx t_f \approx 100 \text{ ps}$) out of the 10 MHz sinusoid in order to minimize the trigger uncertainty of the time interval counters, by not adding any spurious outputs, ie significant noise in the circuit design. These transitions correspond to an equivalent 3-dB bandwidth of approximately 3.5 GHz, considering the response of a simple low-pass filter [7]:

$$t_r := 100 \text{ ps}$$

BW $_{3dB} := \frac{0.35}{t_r}$ BW $_{3dB} = 3.5 \text{ GHz}$

This technique together with noise reduction techniques are reducing the jitter, ie are improving the precision of the time interval measurements [6]. A difficult measurement problem occurs when attempting to make timing measurements such as time interval, in the presence of jitter. Jitter is a common problem in high-speed digital circuits such as those used in satellite laser ranging systems. Traditional measurement algorithms produce highly unstable results in the presence of jitter. Signal averaging being applied in most modern time interval counters to reduce the effects of jitter and yield stable measurement results. However, applying averaging to a signal with jitter has the effect of low-pass filtering the signal, degrading its rise time. The more jitter in the signal, the more the rise time is degraded according to the following expression [8]:

$$t_r := 90 \cdot \text{ps}$$

$$Ji_{pp} := \frac{\sqrt{\ln(2)}}{2 \cdot \pi \cdot \frac{0.35}{t_r}}$$

$$Ji_{pp} = 34 \cdot \text{ps}$$

$$Ji_{rms} := \frac{Ji_{pp}}{6}$$

$$Ji_{rms} = 5.7 \cdot \text{ps}$$

The following measurement shows the rise time of the clock distributor, taken with a high bandwidth digitizing sampling oscilloscope, and the measurement for the standard deviation which is equivalent to the rms jitter. The measured result of about 6 ps is verifying the calculation above.



The test results show clearly that the jitter of the fast NIM outputs is much better than the jitter of the sinusoid outputs (8 ps compared to 43 ps). This is mainly due to its fast transitions of about 100 picoseconds. The jitter on the period measurement (8 ps) can be calculated by the quadratic sum of the jitter on the rise time (6 ps) and the jitter on the fall time (5.5 ps):

$$(6 \cdot \text{ps})^2 + (5.5 \cdot \text{ps})^2 = 8.1 \cdot \text{ps}$$

The calculated value shall be confirmed by the measurement, shown in figure 3.

Figure 3: Period measurement on the 10 MHz NIM output: 8 ps rms

3. Data Analysis

The following analysis shows the rms values of a set of calibration data (rms values for a measured range to a target on the telescope) of a SLR station without the clock distributor (refered to data₁) and after installing the clock distributor (refered to data₂). The data are taken from a ranging system; this implies that all components like laser, telescope, atmosphere, retro-reflector, timing system, detectors are included. The software program MathCAD was used for analyzing the data.

3.1 Analysis of rms values of the calibration data before installing the 10 MHz clock distributor:

3.2 Analysis of rms values of the calibration data <u>after</u> installing the 10 MHz clock distributor:

Figure 4 b: Ranging data analysis of rms values with clock distributor

The standard deviation SD(data₂) of the analyzed rms values (,,standard deviation of the standard deviation") for the measured range has improved by 38 per cent, whereas the mean value meanRMS(data₂) of that calculated standard deviation for the range has improved by 21 per cent, after operating a SLR station with the 10 MHz clock distributor. Basically, the ranging precision has improved.

4. Specification Summary

8 NIM outputs:	
Termination	50 Ω
Voltage level	0 V to -0.8 V
Duty cycle	50 %
Rise time, 20 % to 80 %, typ.	95 ps
Fall time, 20 % to 80 %, typ.	115 ps
Standard deviation on rise time	6.0 ps
Standard deviation on fall time	5.5 ps
Standard deviation on period measurement	8 ps
Standard deviation on Δt measurement	
between 2 different NIM outputs	10 ps

2 sine wave outputs:

Termination	50 Ω
Voltage level	$2.6 V_{pp} \approx 0.9 V_{rms}$
Standard deviation on period measurement	43 ps
Standard deviation on Δt measurement	1
between NIM output and sine wave output	57 ps

Specification on 10 MHz input:

Termination	50 Ω
Voltage level	$0.5 V_{rms}$ to $1.0 V_{rms}$

Power supply:

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+6 Volt	80 mA
–6 Volt	650 mA

The frequency distributor is available with user defined output channels and input frequency. It is installed in a standard NIM module for a Camac Crate and we are able to send a demonstration module to any station which is interested in testing and/or using an excellent frequency distributor.

5. References

- U. L. Rohde, "Microwave and Wireless Synthesizers Theory and Design", John Wiley & Sons, Inc., 1997, ISBN 0-471-52019-5
- 2. J. R. Smith, "Modern Communication Circuits", McGraw-Hill, 1998, ISBN 0-07-059283-7
- 3. W. P. Robins, "Phase Noise in Signal Sources", Peter Peregrinus Ltd., 1982, United Kingdom, ISBN 0-86341-026-X
- 4. C. D. Motchenbacher, J. A. Connelly, "Low-Noise Electronic System Design, John Wiley & Sons, Inc., 1993, ISBN 0-471-57742-1
- 5. H. W. Ott, "Noise Reduction Techniques in Electronic Systems", ISBN 0-471-85068-3
- 6. J. Kölbl, "Frequency Synthesis for Timing Calibration", Proceedings of 11th International Workshop on Laser Ranging, Deggendorf, Germany, 09/1998
- 7. B. Greene, L. Dahl, J. Kölbl, "Femtosecond Timing of Electronic Pulses for SLR", Proceedings of the 10th International Workshop on Laser Ranging Instrumentation, Shanghai, China, 11/1996
- 8. Tektronix, "CSA/11000 Series Statistical Measurements", Technical Note