Patrick/Vejcik/Meyer/Chlebana
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Daq Status

Some of the chronic problems we were having have been fixed.

On 2001.09.22 there was a run lasting 7.5 hours which collected 112.127 $nb^{-1} \rightarrow$ efficiency of 87% for the shift.

Since run 123432 (LERs correctly loaded) we collected 2.9 pb^{-1} (looked for runs with a Physics trigger table and more than $3nb^{-1}$.

Trigger	44.7 nb-1	2.9 pb-1
J20	54,702	3.5 M
J50	743	48 K
J70	2,624	170 K
J100	380	24 K
ST5	62,265	4.0 M
ST10	19,298	1.2 M

For some of the studies we need of the order of 4 M events (or as many events as possible) to have sufficient statistics.

Getting there but still need more events.

Should clarify the mode of operation (data taking vs debugging).

The creeping deadtime problem is understood - waiting for a new version of the FISION library to be built.

Error handling code implemented so that we can recognize when/if the L2 has sync errors. Window appears and the operator has to issue a Halt-Recover-Run (HRR) sequence to resync the L2 trigger.

There was a fairly long run with L2 cutting over the weekend - hopefully we do not have to issue a HRR too frequently.

Working on automating the HRR sequence. This works at some level but needs to be made robust.

Run control seems to issue multiple transitions during the HRR sequence. Changes to the code were made but need to see if the problem is still there.

Error handler does not properly work when you get multiple errors. Being worked on.

Front end code modified to handle Halts for the TDCs when the error line is pulled.

Having problems with using run control to do the central sourcing. State manager not functional and parameters are not consistent. Being work on...

Additions to Run Control to allow one to reset the L2 prescale from the halt state.

Looking into adding a voice alarm when getting various errors during data taking. Will make this general so that the server can accept alarms from different clients.

Hardware event builder has been running well. See some problems associated with when the CSL crashes.

Still have the less than 1% reformatter error problem.

Started preparations to add additional 120 L2 processor nodes during the Oct shutdown. Need some rearrangement of space on the 3rd floor.