

PMC J1 CONNECTOR									
Pin	Pin Name	Signal Name	Pin	Pin Name	Signal Name	Signal Type	PTA Connector Pin Name	Connector	Pin
1	Ground		2	PWR1					
3	Ground		4	PWR2					
5	Ground		6	Ground					
7	GPIO_A1_P_CLK		8	GPIO_A1_N_CLK		unconnected			
9	GPIO_A2_P	Trigger_p	10	GPIO_A2_N	Trigger_n	LVDS INPUT PAIR	JN1_S13	JN1	28
11	Ground		12	Ground					
13	GPIO_A3_P	ADC0_p	14	GPIO_A3_N	ADC0_n	LVDS INPUT PAIR	JN2_S0	JN2	2
15	GPIO_A4_P	ADC1_p	16	GPIO_A4_N	ADC1_n	LVDS INPUT PAIR	JN2_S1	JN2	3
17	GPIO_A5_P	ADC2_p	18	GPIO_A5_N	ADC2_n	LVDS INPUT PAIR	JN2_S2	JN2	4
19	GPIO_A6_P	ADC3_p	20	GPIO_A6_N	ADC3_n	LVDS INPUT PAIR	JN2_S3	JN2	5
21	Ground		22	Ground					
23	GPIO_A7_P	ADC4_p	24	GPIO_A7_N	ADC4_n	LVDS INPUT PAIR	JN2_S4	JN2	8
25	GPIO_A8_P	ADC5_p	26	GPIO_A8_N	ADC5_n	LVDS INPUT PAIR	JN2_S5	JN2	9
27	GPIO_A9_P	ADC6_p	28	GPIO_A9_N	ADC6_p	LVDS INPUT PAIR	JN2_S6	JN2	10
29	GPIO_A10_P	ADC7_p	30	GPIO_A10_N	ADC7_n	LVDS INPUT PAIR	JN2_S7	JN2	13
31	Ground		32	Ground					
33	GPIO_A11_P	COL0_p	34	GPIO_A11_N	COL0_n	LVDS INPUT PAIR	JN2_S8	JN2	17
35	GPIO_A12_P	COL1_p	36	GPIO_A12_N	COL1_n	LVDS INPUT PAIR	JN2_S9	JN2	19
37	GPIO_A13_P	COL2_p	38	GPIO_A13_N	COL2_n	LVDS INPUT PAIR	JN2_S10	JN2	20
39	GPIO_A14_P	COL3_p	40	GPIO_A14_N	COL3_n	LVDS INPUT PAIR	JN2_S11	JN2	22
41	Ground		42	Ground					
43	GPIO_A15_P	CHIP_OR_p	44	GPIO_A15_N	CHIP_OR_n	LVDS INPUT PAIR	JN1_S11	JN1	26
45	GPIO_A16_P	TO_C_p	46	GPIO_A16_N	TO_C_p	LVDS INPUT PAIR	JN1_S27	JN1	52
47	GPIO_A17_P	SHIFTMEM0_p	48	GPIO_A17_N	SHIFTMEM0_n	LVDS INPUT PAIR	JN1_S28	JN1	53
49	Ground		50	Ground					

PMC J2 CONNECTOR									
Pin	Pin Name	Signal Name	Pin	Pin Name	Signal Name	Signal Type	PTA Connector Pin Name	Connector	Pin
1	Ground		2	PWR1					
3	Ground		4	PWR2					
5	Ground		6	Ground					
7	GPIO_B1_P_CLK	ADC_CLKp	8	GPIO_B1_N_CLK	ADC_CLKn	LVDS OUTPUT PAIR	JN1_S29	JN1	54
9	GPIO_B2_P	CLKAN_p	10	GPIO_B2_N	CLKAN_n	LVDS OUTPUT PAIR	JN1_S34	JN1	61
11	Ground		12	Ground					
13	GPIO_B3_P	BCOCLK_p	14	GPIO_B3_N	BCOCLK_n	LVDS OUTPUT PAIR	JN1_S31	JN1	58
15	GPIO_B4_P	SCLK_p	16	GPIO_B4_N	SCLK_n	LVDS OUTPUT PAIR	JN1_S6	JN1	12
17	GPIO_B5_P	ROW0_p	18	GPIO_B5_N	ROW0_n	LVDS INPUT PAIR	JN2_S12	JN2	23
19	GPIO_B6_P	ROW1_p	20	GPIO_B6_N	ROW1_n	LVDS INPUT PAIR	JN2_S13	JN2	25
21	Ground		22	Ground					
23	GPIO_B7_P	ROW2_p	24	GPIO_B7_N	ROW2_n	LVDS INPUT PAIR	JN2_S14	JN2	26
25	GPIO_B8_P	ROW3_p	26	GPIO_B8_N	ROW3_n	LVDS INPUT PAIR	JN2_S15	JN2	28
27	GPIO_B9_P	ROW4_p	28	GPIO_B9_N	ROW4_n	LVDS INPUT PAIR	JN2_S16	JN2	29
29	GPIO_B10_P	ROW5_p	30	GPIO_B10_N	ROW5_n	LVDS INPUT PAIR	JN2_S17	JN2	31
31	Ground		32	Ground					
33	GPIO_B11_P	CRESET_p	34	GPIO_B11_N	CRESET_n	LVDS OUTPUT PAIR	JN1_S20	JN1	41
35	GPIO_B12_P	TO_ADV_p	36	GPIO_B12_N	TO_ADV_n	LVDS OUTPUT PAIR	JN1_S12	JN1	27
37	GPIO_B13_P	TIG_p	38	GPIO_B13_N	TIG_n	LVDS OUTPUT PAIR	JN1_S16	JN1	33
39	GPIO_B14_P	LD_KILL_p	40	GPIO_B14_N	LD_KILL_n	LVDS OUTPUT PAIR	JN1_S26	JN1	49
41	Ground		42	Ground					
43	GPIO_B15_P	SHIFT_IN_p	44	GPIO_B15_N	SHIFT_IN_n	LVDS OUTPUT PAIR	JN1_S18	JN1	37
45	GPIO_B16_P	SHIFTMEMINp	46	GPIO_B16_N	SHIFTMEMINn	LVDS OUTPUT PAIR	JN1_S24	JN1	47
47	GPIO_B17_P	DACS_p	48	GPIO_B17_N	DACS_n	LVDS OUTPUT PAIR	JN1_S3	JN1	6
49	Ground		50	Ground					