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DATE: January 24, 2002

TO: DPSD Business Unit

CC: IPG / DPSD BIOS Engineering

FROM: IPG / DPSD BIOS Engineering

SUBJECT:

About This Release

- **BIOS String:** PT84520A.86A.0015.P08.0302261328
- UNDI PXE Version: ICH2 PXE 2.1 Build 083

Features/Errata Fixed in This Release

P08-0015

• Fixed the issue with booting using 2.0Ghz processor.

P07-0013

- Updated Intel Copyright Date to 2003
- Updated processor frequency display in POST and in BIOS Setup.

P07-0012

- Intel(R) Boot Agent FE v4.1.09
- Fixed issue where system will not wake from S1 if the keystroke is hit before system enters S1 standby.
- Fixed issue where flashing the previous BIOS through EBU in Microsoft* Windows XP* generated CMOS checksum errors.
- Updated from AMI Core 08.00.05 to 08.00.07.
- Added new BBS codes to preserve the boot order.
- Added the update for System Configuration Data.
- Added Hardware Monitoring screen for setup.
- Added BMI 1.1 support.
- Fixed issue where flashing the previous BIOS through EBU in Windows XP generated CMOS checksum errors.
- Disabled Hyper-Threading technology support.
- Fixed a potential failure after CMOS corruption when incorrect CUSTOM CMOS defaults were installed.
- Fixed password is required to enter maintenance mode issue.
- Fixed PXE base ROM dispatching issues.
- Fixed issue during SMBus ARP with certain memory installed.
- Removed Wake On Modem Ring question in BIOS setup.

P06-0011

- Updated Intel Boot Agent to 4.1.08.
- Added fix to correctly preserve register 70/74 across SMIs.
- Updated SMBIOS TYPE 8 connector information.
- Added Hyper-Threading Technology enable/disable switch in setup.

- Added ASF support.
- Added support for Intel® Integrator Toolkit 2.0 and BMI 1.1.
- Fixed an issue of incorrect "Block mode" display of Secondary Slave on the IDE configuration screen.
- Added the ability to enable and disable S.M.A.R.T. hard disk drive capabilities.
- Fixed an issue where USB mouse is slow to respond coming out of S3 in Microsoft* Windows* 2000.
- Fixed the loss of USB mouse upon resume from S4 under Windows 2000
- Added Hyper-Threading support.
- Fixed a potential system hang problem after CMOS corruption and incorrect CUSTOM CMOS defaults were installed.
- Fixed the problem where BMI runtime interface could not return the correct FAN thermal data.
- Added the hard-disk pre-delay functionality.
- Added support for BIOS IDE detection to be more tolerant of drive configurations with slower spin-up times.

P05-0010

- Added ASF support.
- Fixed issue where Windows 2000 would reload hardware profile after entering BIOS.
- Added Chassis Intrusion question in Setup.
- Updated support for Intel® Integrator Toolkit (ITK).
- Added support for future processors.
- Fixed a potential CMOS checksum error problem after BIOS update.
- Added the workaround to support backward compatibility in recovery with BL010100 BIOS.
- Fixed installation hang with Windows .NET when UDMA/133 drives are attached to the system.
- Updated System Configuration Data.

P04-0009

- Removed power management option for PS2 keyboard and mouse. PS2 keyboard and mouse are now always enabled to participate on wake-up events from ACPI S1 and S3 modes.
- Corrected Device Locator field and Bank Locator field of Type 17 SMBIOS structure.
- Fixed an issue where ECC option is not being displayed in setup while in manufacturing mode.
- Added BIOS workaround for a Windows 98SE issue where ATAPI devices are not reprogrammed on resume from Suspend-to-RAM (S3).
- Fixed an issue where the bus numbers for devices and PCI slots sitting behind the PCI-to-PCI bridge in the ICH4 were not being saved correctly.
- Fixed an issue where in some configurations, Full speed USB devices attached to a USB hub were not properly detected.

P03-0007

- Fixed an issue where the system would hang coming out of ACPI S3 resume, if USB legacy was disabled in setup.
- Added help text for the ISA Enable Bit setup option.
- Fixed issue where the system would not boot to or read from an ARMD (ATAPI Removable) device when it is configured as a slave device.
- Fixed an issue where POST does not display all the installed option ROMs.
- Disabled functionality of the F8 and F12 POST hotkeys, if a supervisor password has been installed and the user access level is not set to "full".
- Fixed POST hang issue with an AGP video card, when the LAN/PXE extension is enabled.
- Fixed issue where system would power back on immediately after the power button was pressed if "Wake on Ring" was enabled.
- Fixed SMBIOS table information for the Intel® Pentium® 4 and Intel® Celeron® processors

P02-0006

• Initial BIOS release.

Known Errata with This Release