

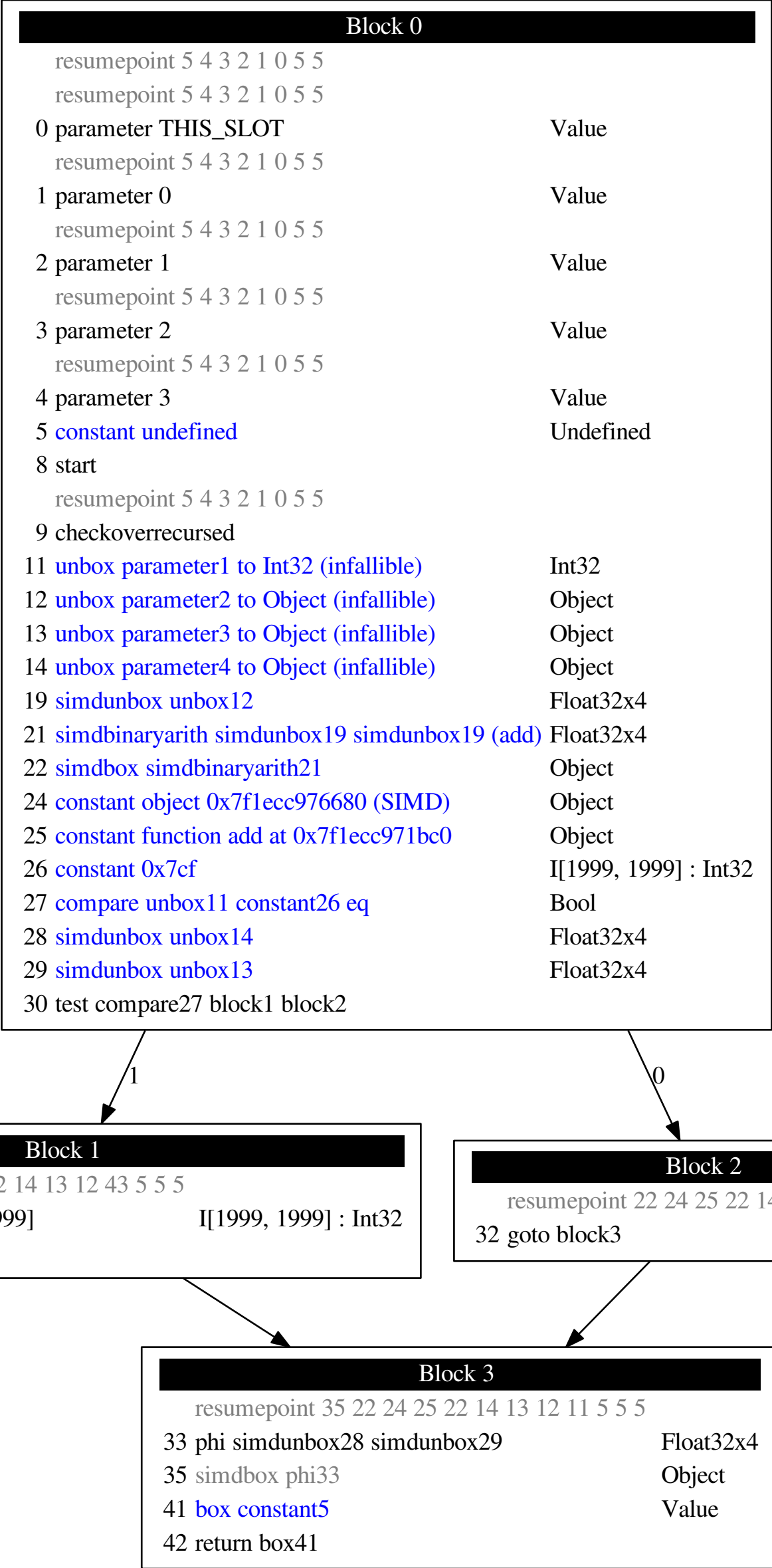
Block 0		
resumepoint 5 4 3 2 1 0 5 5		
resumepoint 5 4 3 2 1 0 5 5		
0 parameter THIS_SLOT	Value	
resumepoint 5 4 3 2 1 0 5 5		
1 parameter 0	Value	
resumepoint 5 4 3 2 1 0 5 5		
2 parameter 1	Value	
resumepoint 5 4 3 2 1 0 5 5		
3 parameter 2	Value	
resumepoint 5 4 3 2 1 0 5 5		
4 parameter 3	Value	
5 constant undefined	Undefined	
8 start		
resumepoint 5 4 3 2 1 0 5 5		
9 checkoverrecursed		
11 unbox parameter1 to Int32 (infallible)	Int32	
12 unbox parameter2 to Object (infallible)	Object	
13 unbox parameter3 to Object (infallible)	Object	
14 unbox parameter4 to Object (infallible)	Object	
19 simdunbox unbox12	Float32x4	
21 simdbinaryarith simdunbox19 simdunbox19 (add)	Float32x4	
22 simdbox simdbinaryarith21	Object	
24 constant object 0x7f1ecc976680 (SIMD)	Object	
25 constant function add at 0x7f1ecc971bc0	Object	
26 constant 0x7cf	Int32	
27 compare unbox11 constant26 eq	Bool	
28 simdunbox unbox14	Float32x4	
29 simdunbox unbox13	Float32x4	
30 test compare27 block1 block2		

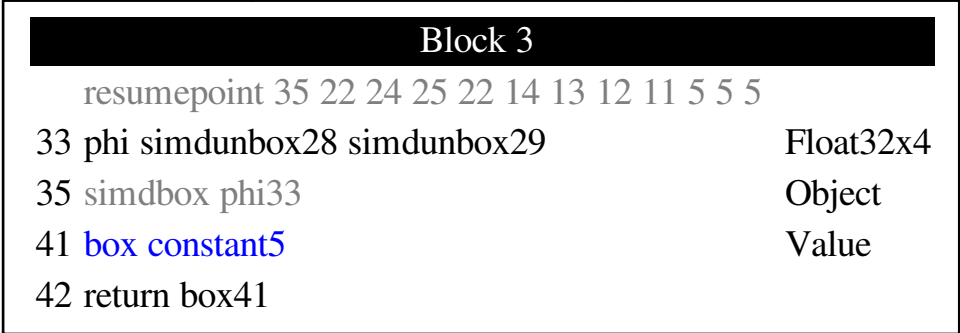
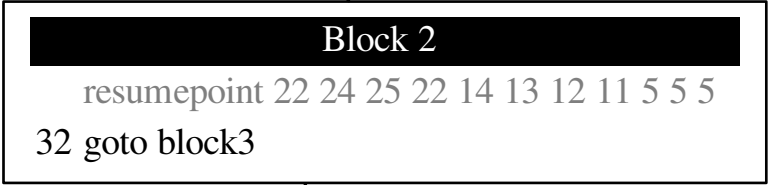
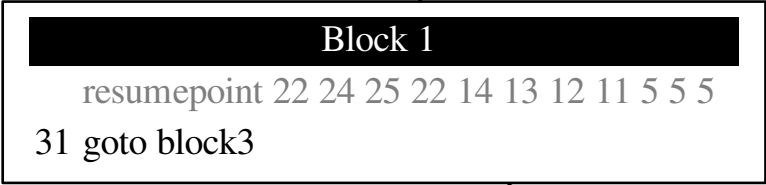
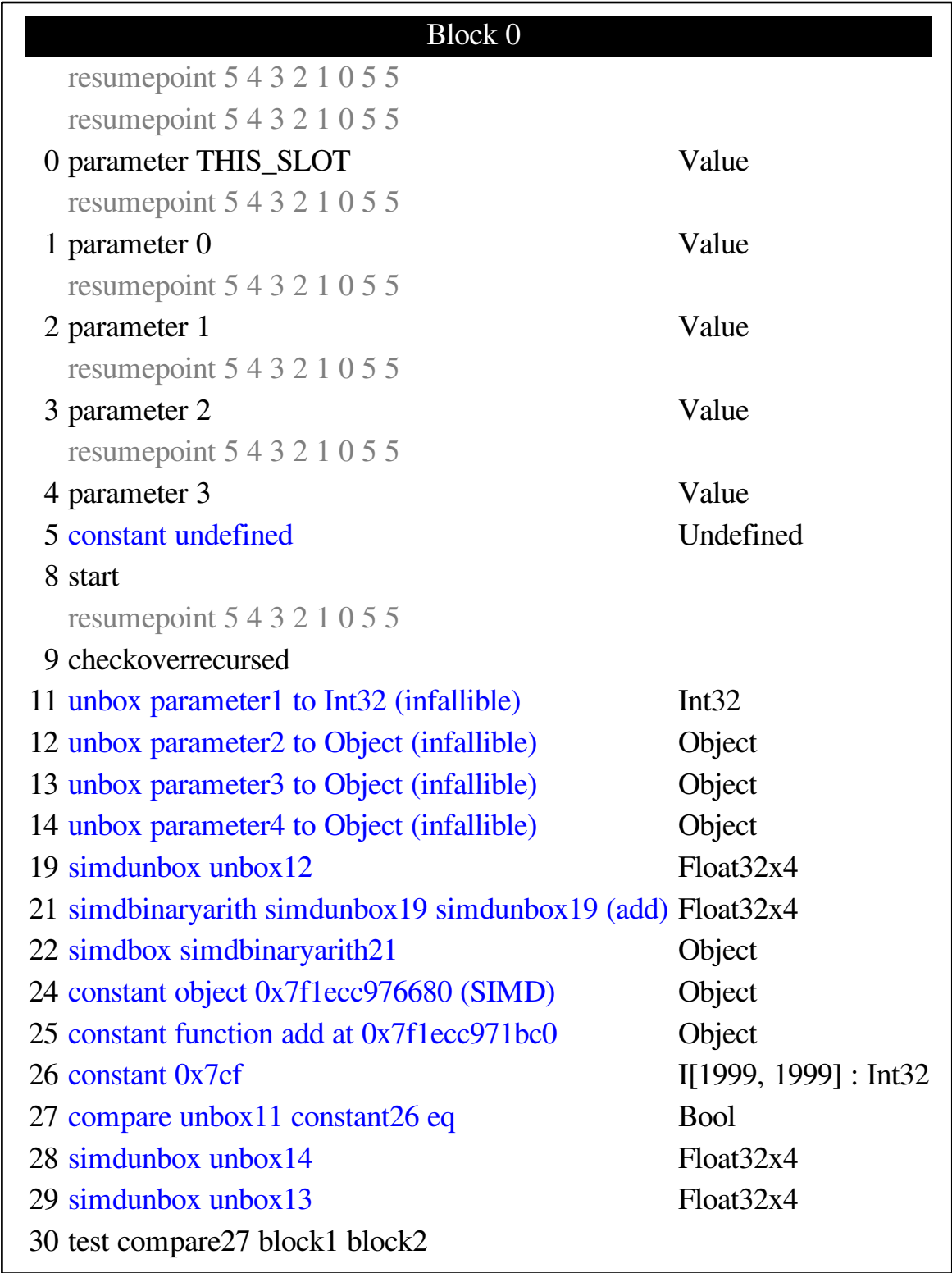


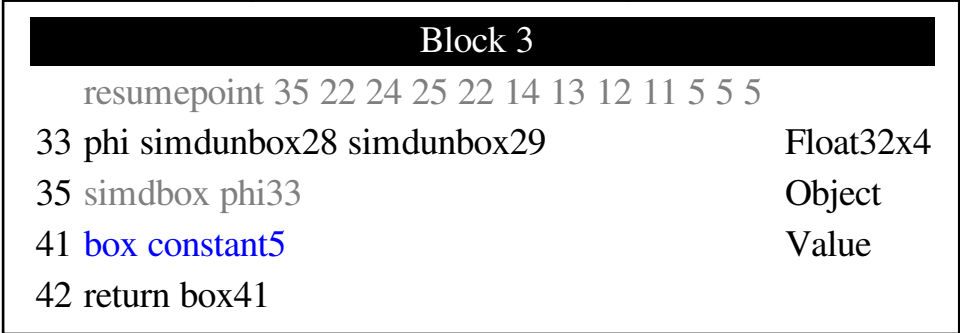
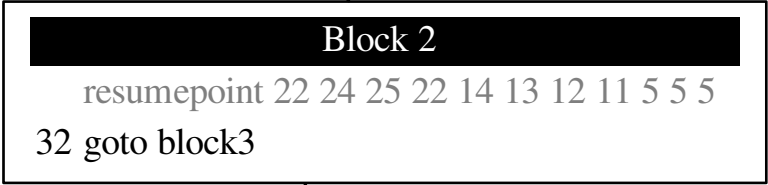
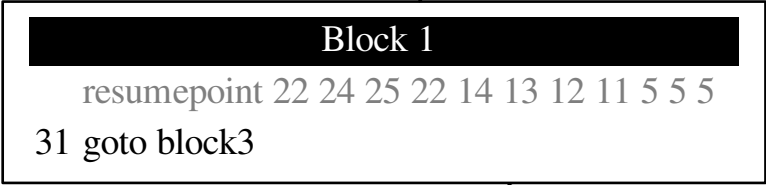
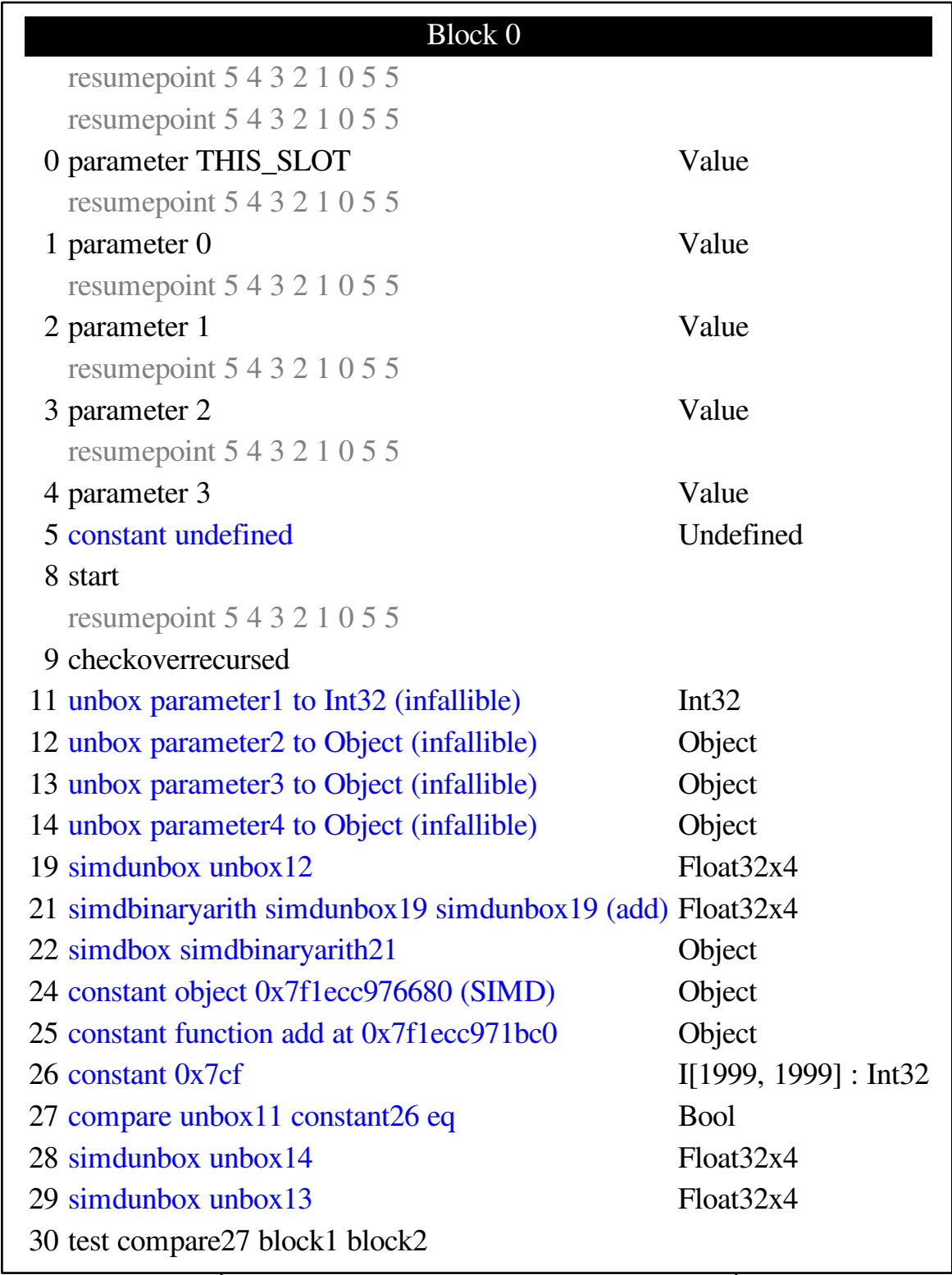
Block 1		
resumepoint 22 24 25 22 14 13 12 43 5 5 5		
43 beta unbox11 I[1999, 1999]	Int32	
31 goto block3		

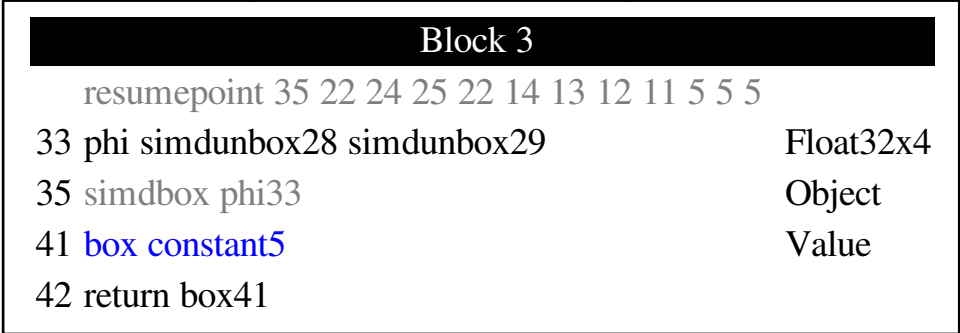
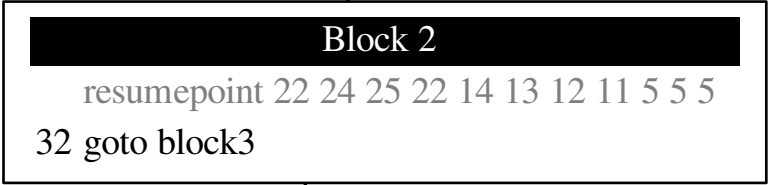
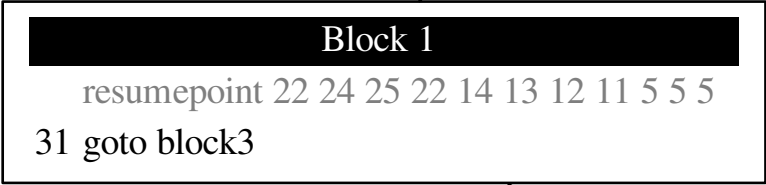
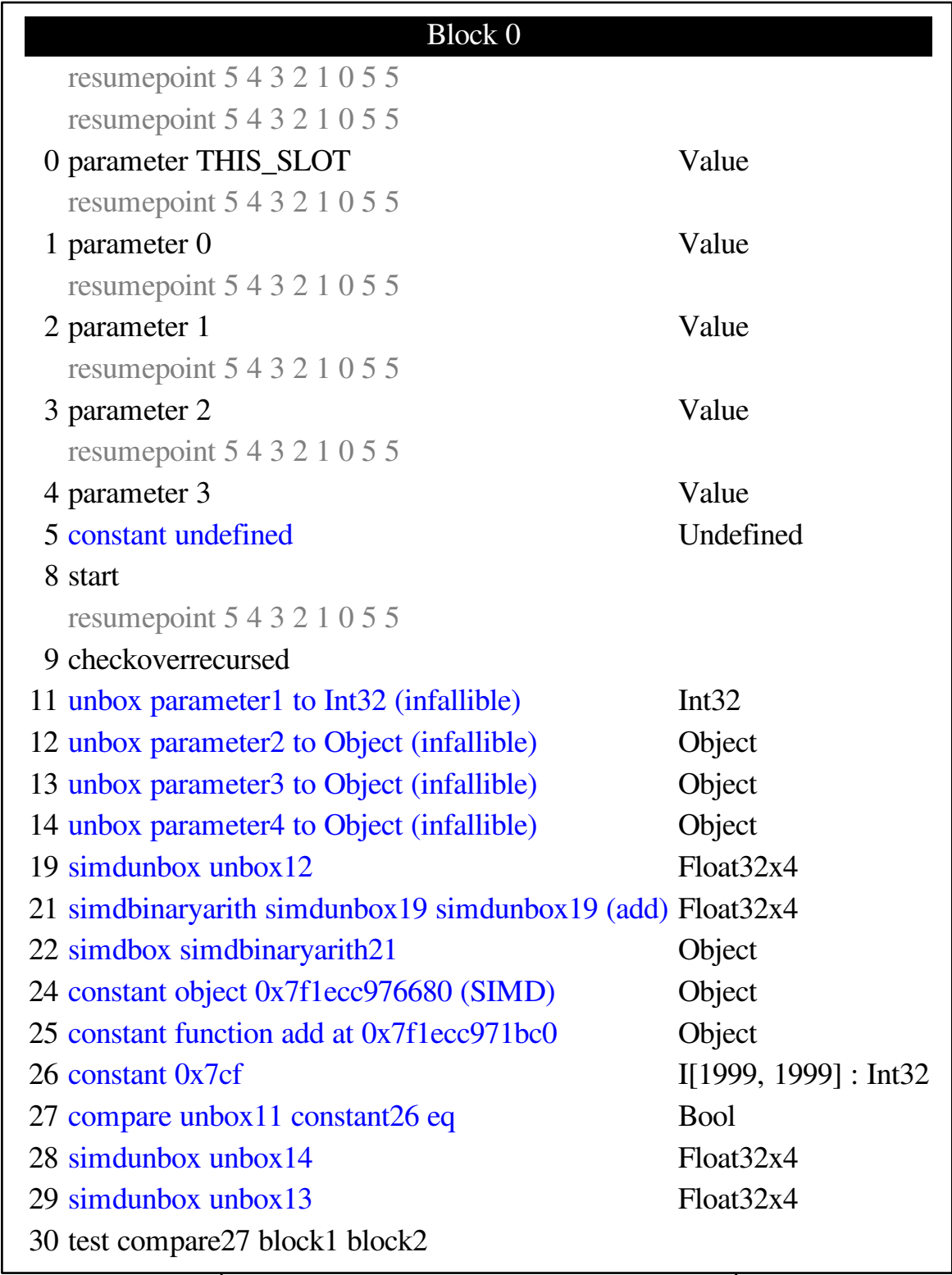
Block 2		
resumepoint 22 24 25 22 14 13 12 11 5 5 5		
32 goto block3		

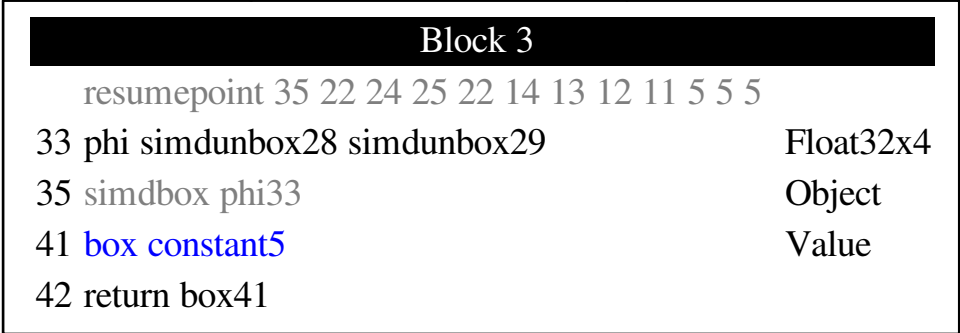
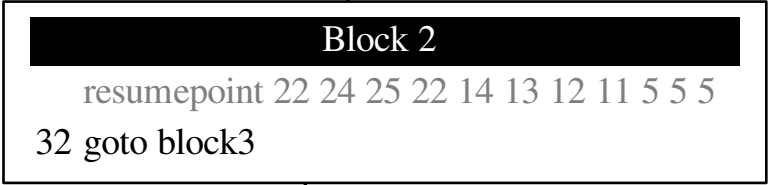
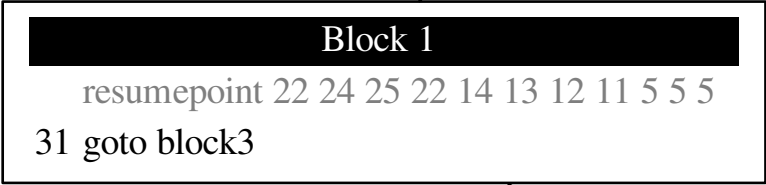
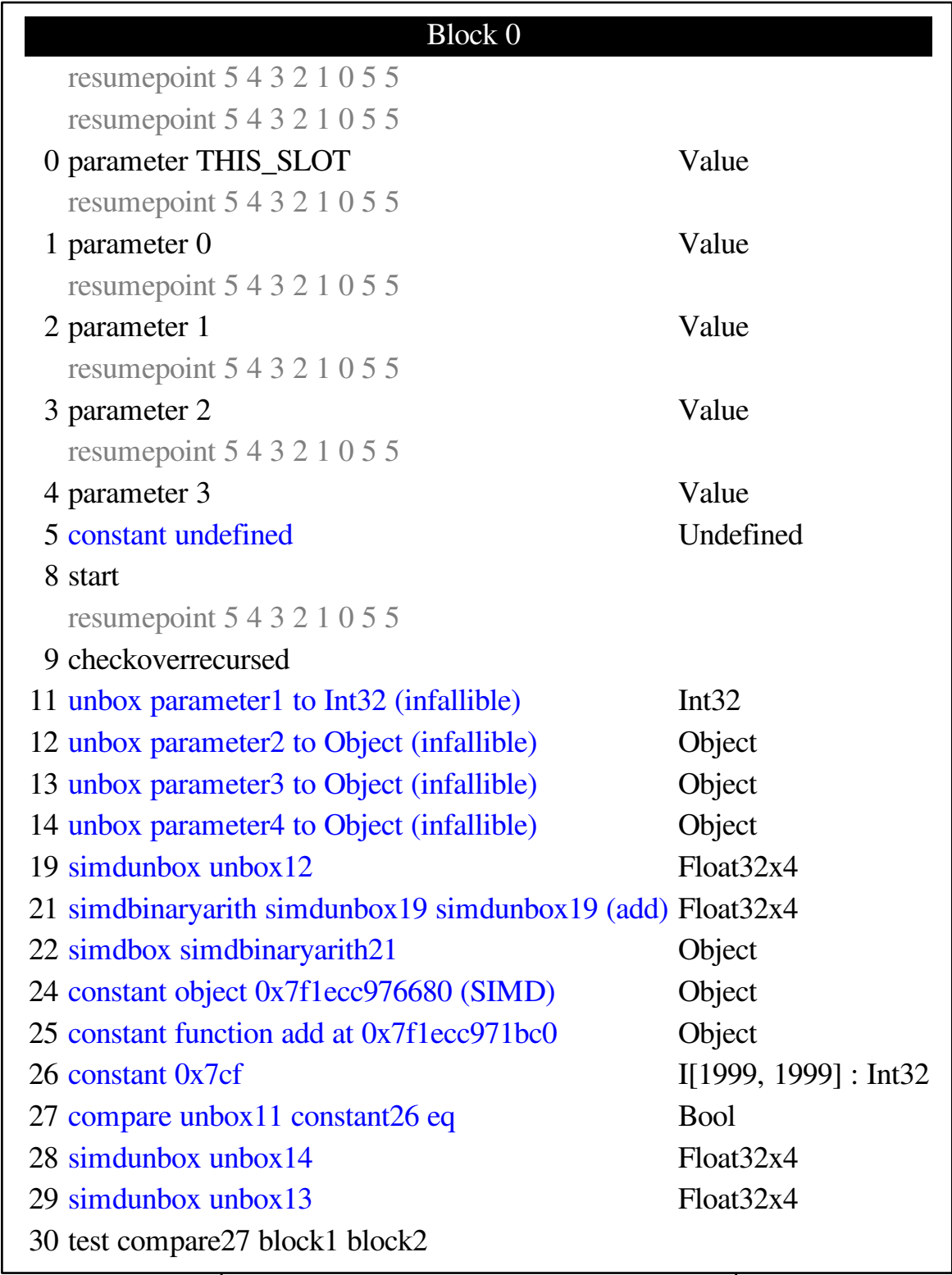
Block 3		
resumepoint 35 22 24 25 22 14 13 12 11 5 5 5		
33 phi simdunbox28 simdunbox29	Float32x4	
35 simdbox phi33	Object	
41 box constant5	Value	
42 return box41		

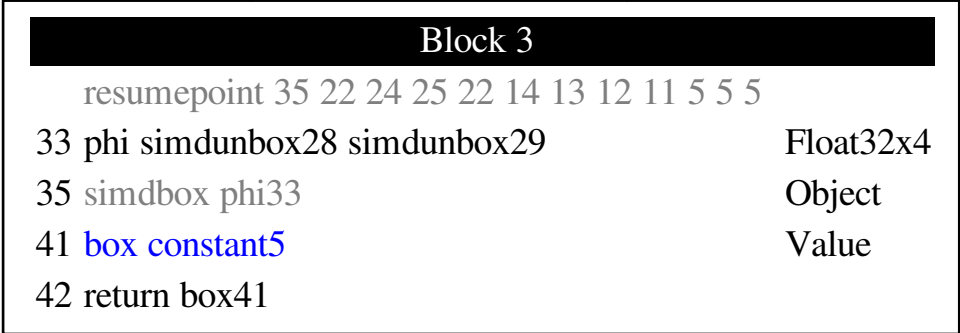
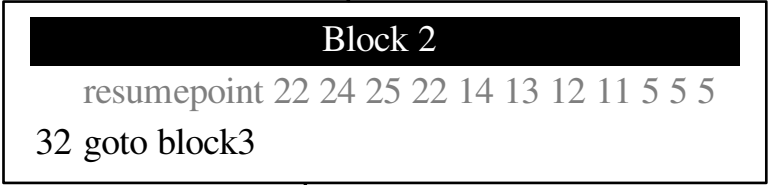
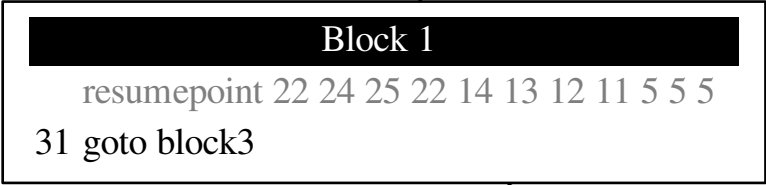
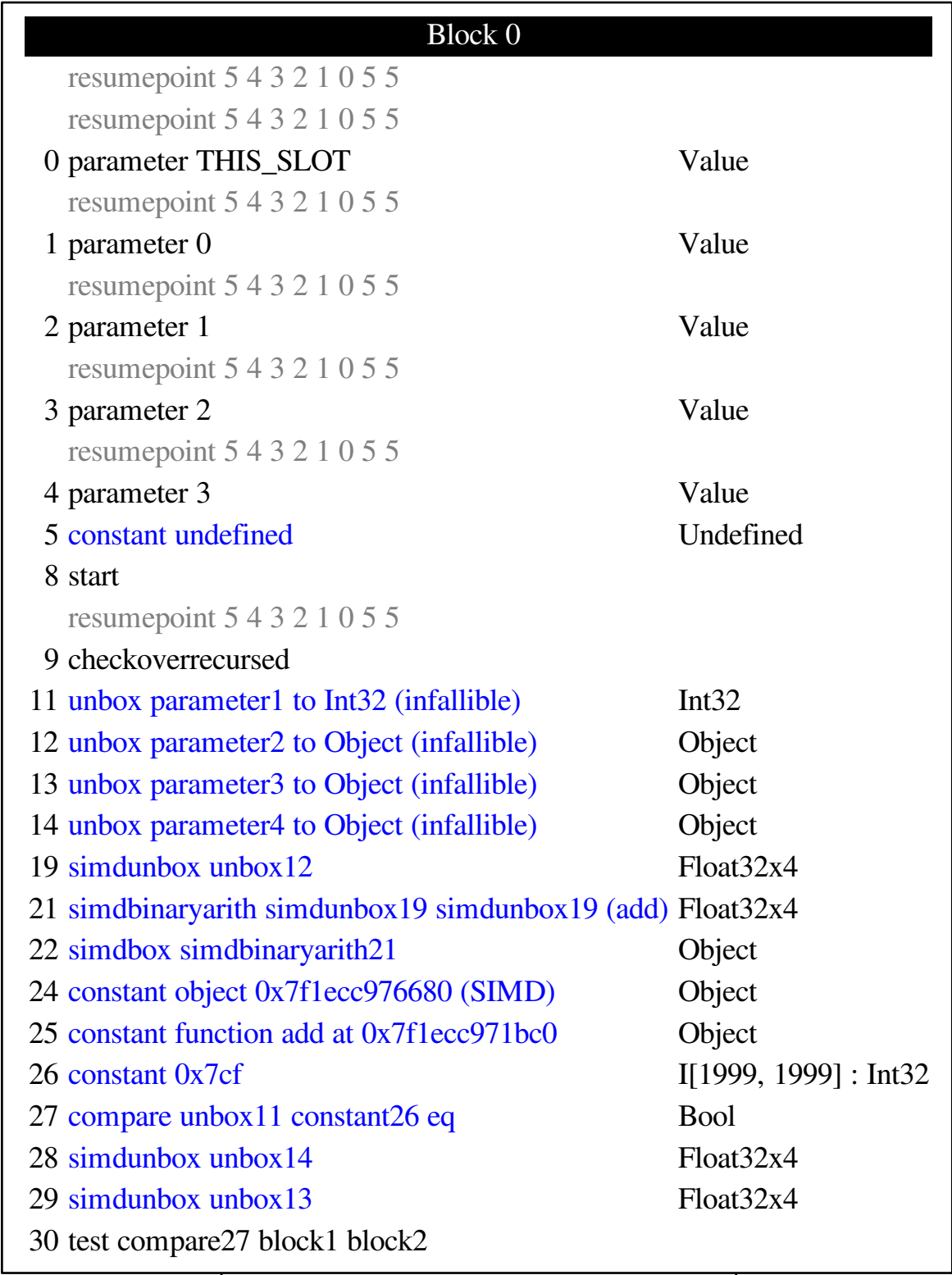


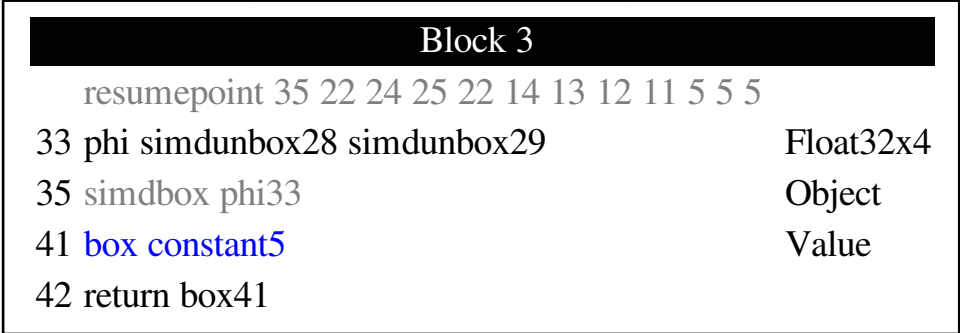
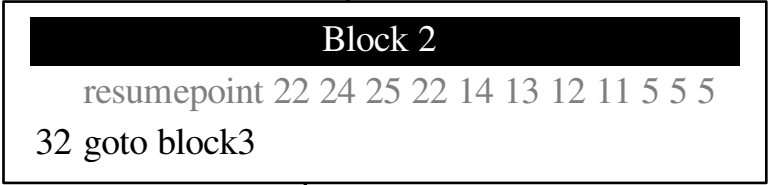
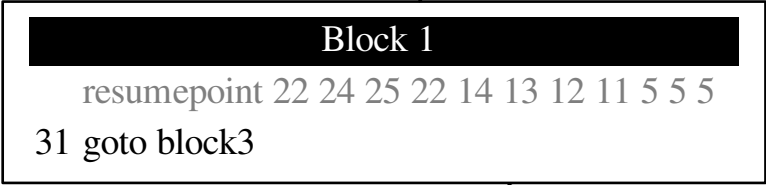
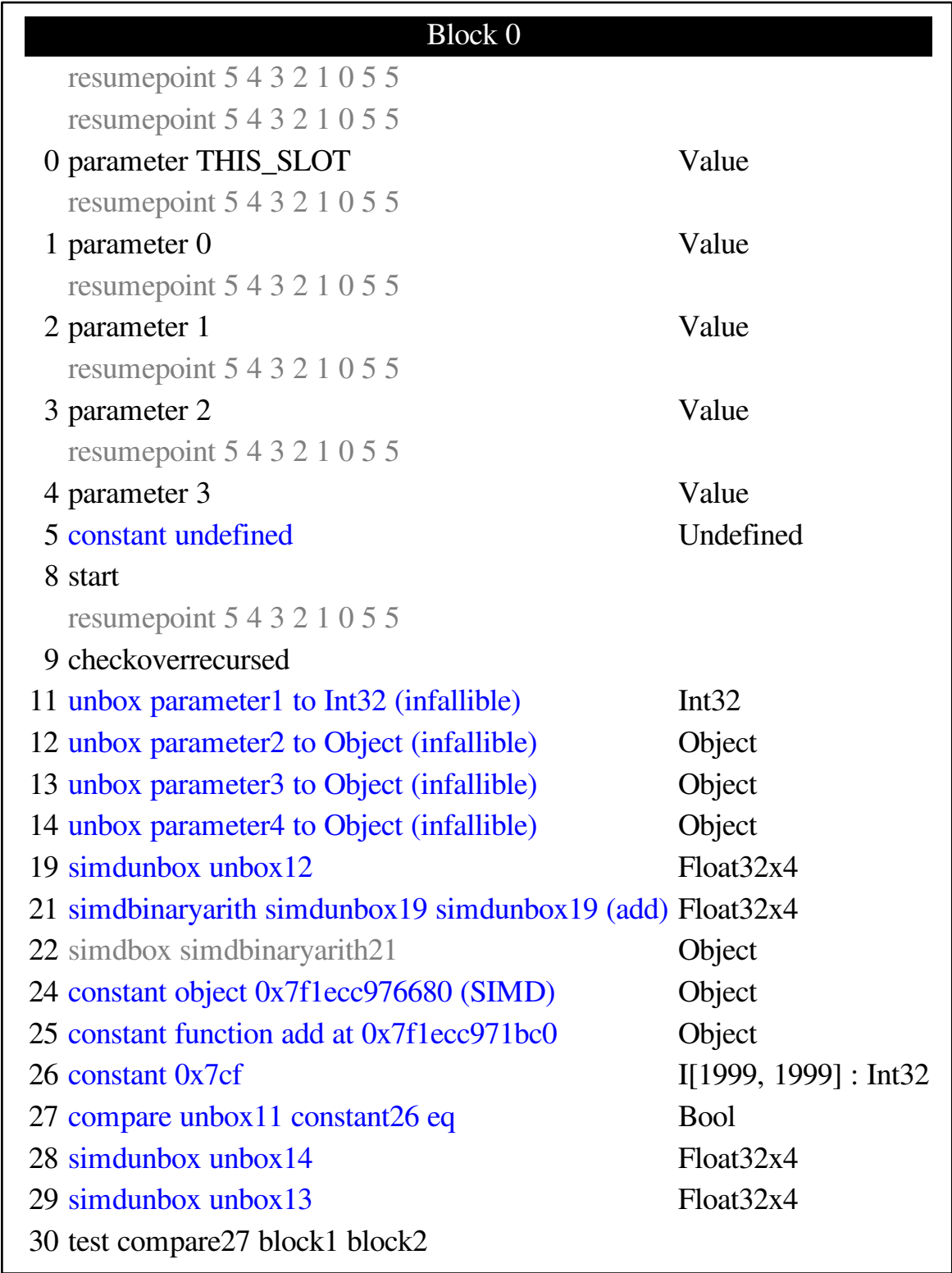


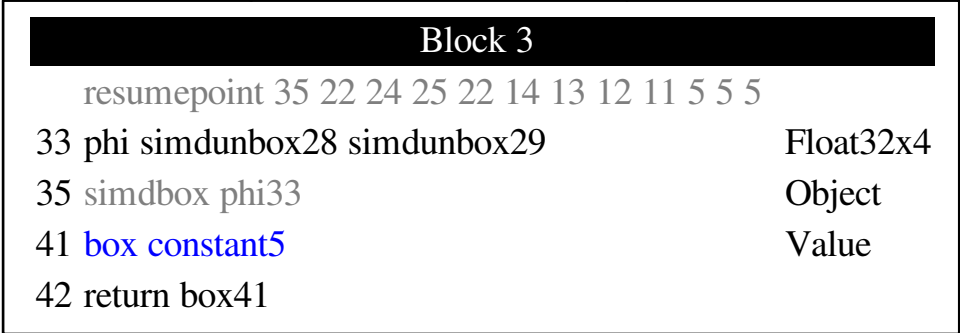
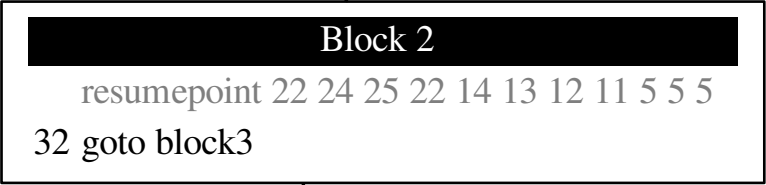
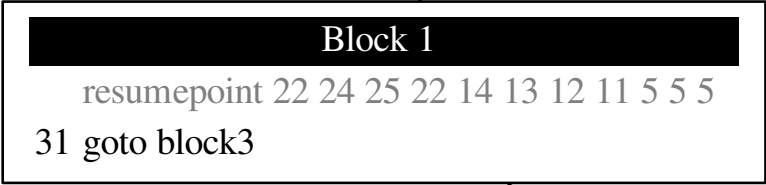
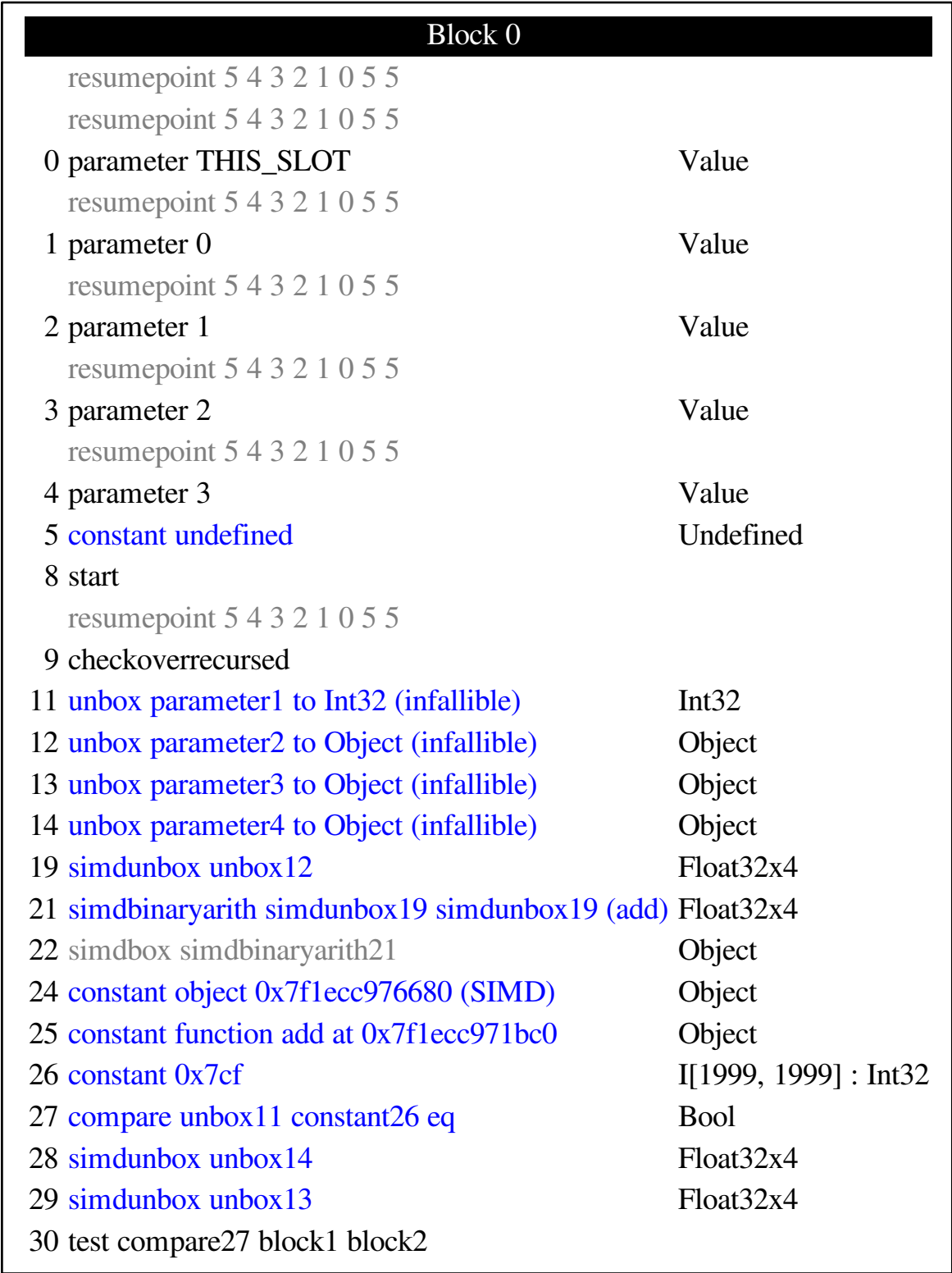


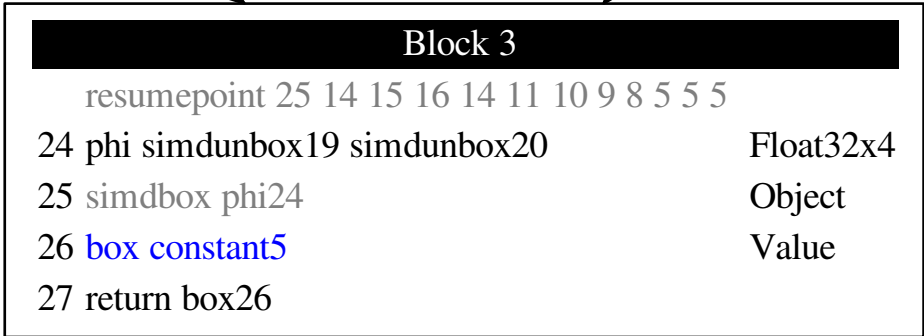
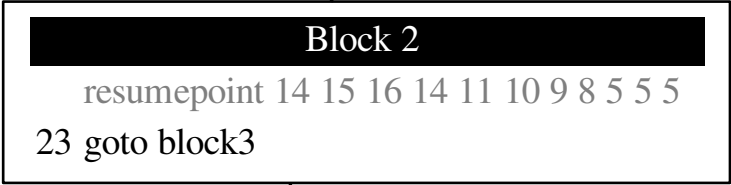
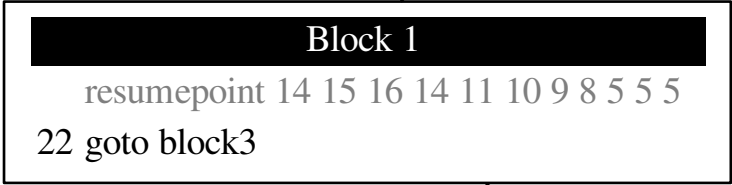
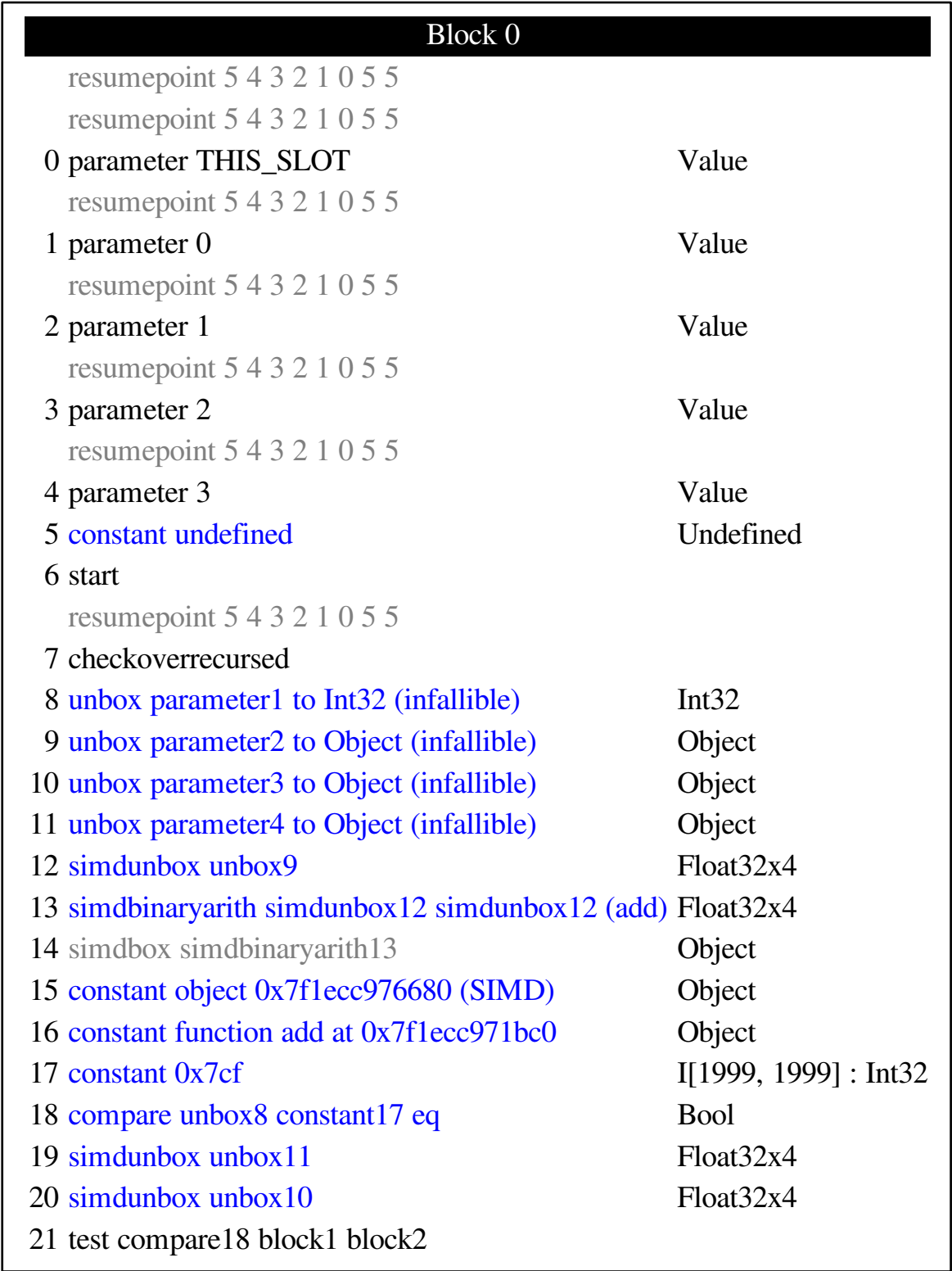


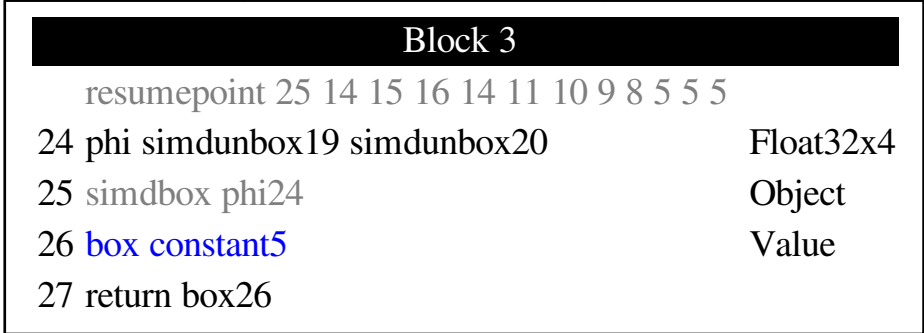
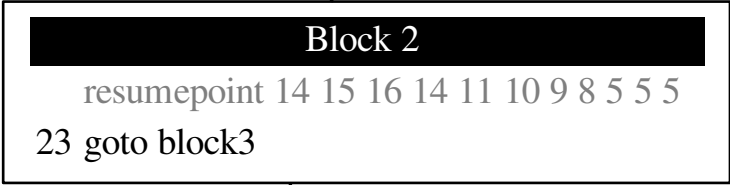
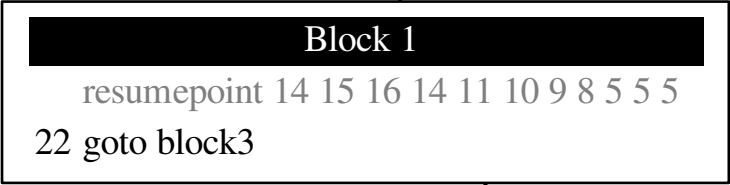
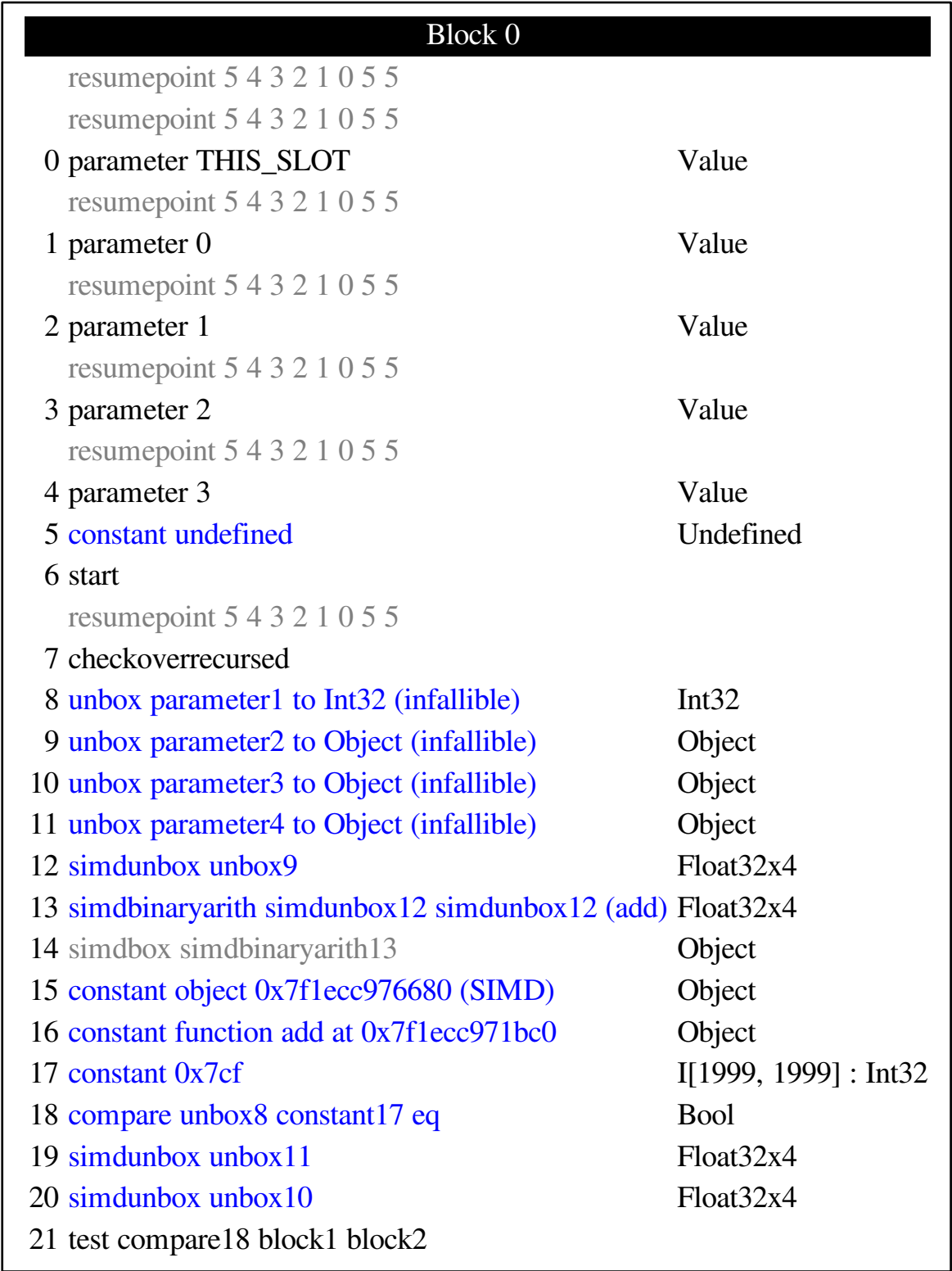






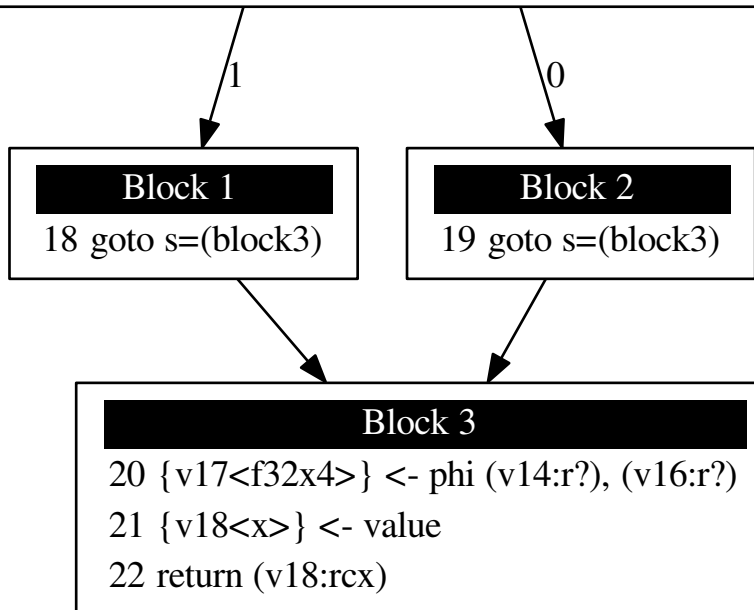






Block 0

```
1 {v1<x>:arg:0} <- parameter
2 {v2<x>:arg:8} <- parameter
3 {v3<x>:arg:16} <- parameter
4 {v4<x>:arg:24} <- parameter
5 {v5<x>:arg:32} <- parameter
6 start
7 checkoverrecursed
8 osipoint
9 {v6<i>} <- unbox (v2:r?)
10 {v7<o>} <- unbox (v3:r?)
11 {v8<o>} <- unbox (v4:r?)
12 {v9<o>} <- unbox (v5:r?)
13 {v11<f32x4>} <- simdunbox (v7:r) t=(v10<g>)
14 {v12<f32x4>:tied(0)} <- simdbinaryarithfx4 (v11:r), (v11:r?) t=(bogus)
15 {v14<f32x4>} <- simdunbox (v9:r) t=(v13<g>)
16 {v16<f32x4>} <- simdunbox (v8:r) t=(v15<g>)
17 compareandbranch (v6:r), (c) s=(block1, block2)
```



Block 0

```
1 {v1<x>:arg:0} <- parameter
2 {v2<x>:arg:8} <- parameter
3 {v3<x>:arg:16} <- parameter
4 {v4<x>:arg:24} <- parameter
5 {v5<x>:arg:32} <- parameter
6 start
7 checkoverrecursed
8 osipoint
9 {v6<i>:rax} <- unbox (arg:8)
10 {v7<o>:rbx} <- unbox (arg:16)
11 {v8<o>:rcx} <- unbox (arg:24)
12 {v9<o>:rdx} <- unbox (arg:32)
13 {v11<f32x4>:%xmm0.s4} <- simdunbox (rbx) t=(v10<g>:rbp)
14 {v12<f32x4>:%xmm0.s4} <- simdbinaryarithfx4 (%xmm0.s4), (%xmm0.s4) t=(bogus)
15 {v14<f32x4>:%xmm0.s4} <- simdunbox (rdx) t=(v13<g>:rbx)
16 {v16<f32x4>:%xmm1.s4} <- simdunbox (rcx) t=(v15<g>:rdx)
17 compareandbranch (rax), (c) s=(block1, block2)
```

1

Block 1

```
18 goto s=(block3)
```

0

Block 2

```
0 movegroup [%xmm1.s4 -> %xmm0.s4, f32x4]
19 goto s=(block3)
```

Block 3

```
20 {v17<f32x4>:%xmm0.s4} <- phi (v14:r?), (v16:r?)
21 {v18<x>:rcx} <- value
22 return (rcx)
```